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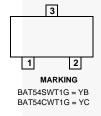


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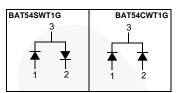
BAT54SWT1G / BAT54CWT1G Schottky Diodes



SOT-323



Connection Diagram



Ordering Information

Part Number	Top Mark	Package	Packing Method
BAT54SWT1G	YB	SC70 3L (SOT-323)	Tape and Reel
BAT54CWT1G	YC	SC70 3L (SOT-323)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage	30	V
I _{F(AV)}	Average Rectified Forward Current	200	mA
I _{FSM}	Non-Repetitive Peak Forward Surge Current Pulse Width = 1.0 second	600	mA
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _J	Operating Junction Temperature	-65 to +125	°C

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P_{D}	Power Dissipation	232	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ⁽¹⁾	430	°C/W

Note:

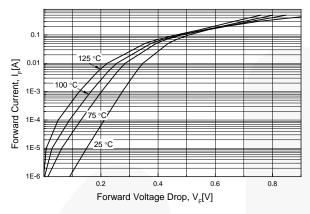
1. FR-4 board $(3.0 \times 4.5 \times 0.062"$ by $1.0 \times 0.5"$ land pads)

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _R	Breakdown Voltage	$I_R = 10 \mu A$	30		V
V _F	Forward Voltage	I _F = 0.1 mA		240	mV
		I _F = 1 mA		320	
		I _F = 10 mA		400	
		I _F = 30 mA		500	
		I _F = 100 mA		800	
I _R	Reverse Leakage	V _R = 25 V		2	μΑ
C _T	Total Capacitance	V _R = 1 V, f = 1.0 MHz	1	10	pF
t _{rr}	Reverse Recovery Time	$I_F = I_R = 10 \text{ mA}, I_{RR} = 1.0 \text{ mA}, R_L = 100 \Omega$		5.0	ns

Typical Performance Characteristics



1E-3

1E-4

1E-4

1E-5

1E-6

1E-7

1E-8

Figure 1. Forward Voltage vs. Temperature

Figure 2. Reverse Leakage Current vs. Temperature

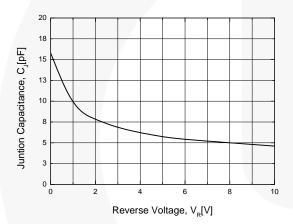
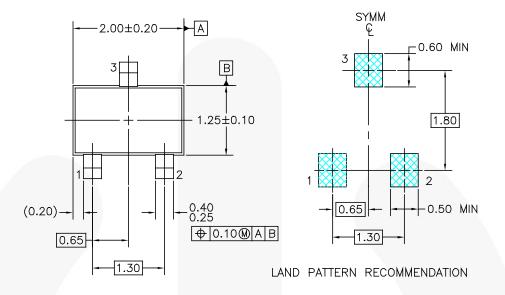
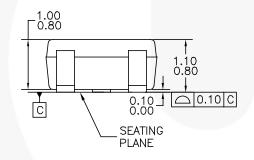
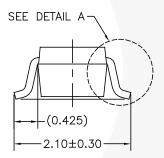


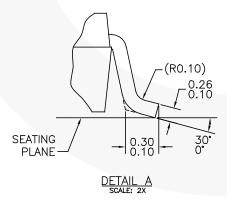
Figure 3. Capacitance vs. Reverse Bias Voltage

Physical Dimensions









NOTES: UNLESS OTHERWISE SPECIFIED

- THIS PACKAGE CONFORMS TO EIAJ
- ALL DIMENSIONS ARE IN MILLIMETERS.
 DIMENSIONS DO NOT INCLUDE BURRS
 OR MOLD FLASH.

Figure 4. 3-LEAD, SC70, EIAJ SC-70, 1.25MM WIDE





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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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