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FDA28N50F N-Channel UniFETTM FRFET[®] MOSFET **500 V, 28 A, 175 m**Ω

Features

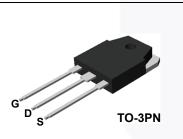
- R_{DS(on)} = 140 mΩ (Typ.) @ V_{GS} = 10 V, I_D = 14 A
- Low Gate Charge (Typ. 80 nC)
- Low C_{rss} (Typ. 38 pF)
- 100% Avalanche Tested
- · Improved dv/dt Capability
- · RoHS Compliant

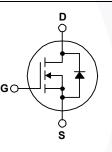
Applications

- PDP TV
- Uninterruptible Power Supply
- · AC-DC Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET® MOSFET has been enhanced by lifetime control. Its trr is less than 100nsec and the reverse dv/dt immunity is 15V/ns while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		FDA28N50F	Unit			
V _{DSS}	Drain to Source Voltage	500	V			
V _{GSS}	Gate to Source Voltage			±30	V	
ID	Drain Current	- Continuous (T _C = 25 ^o C)		28		
		- Continuous (T _C = 100 ^o C)		17	Α	
I _{DM}	Drain Current	- Pulsed	(Note 1)	112	А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)			2352	mJ	
I _{AR}	Avalanche Current		(Note 1)	28	А	
E _{AR}	Repetitive Avalanche Energy	ду	(Note 1)	31	mJ	
dv/dt	Peak Diode Recovery dv/d	t	(Note 3)	20	V/ns	
P _D	Dewer Dissignation	(T _C = 25 ^o C)		310	W	
	Power Dissipation	- Derate Above 25°C		2.5	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C	
TI	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C	

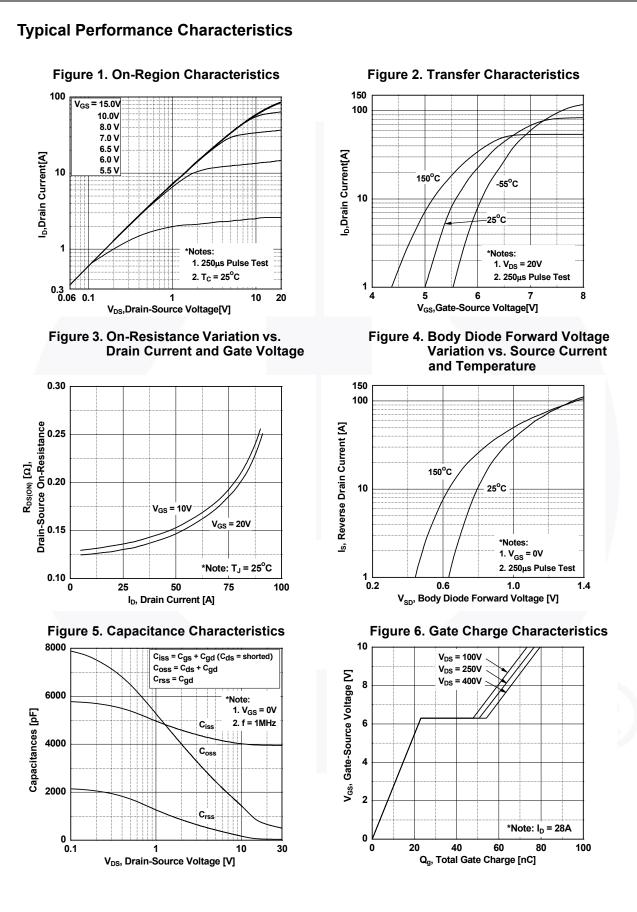
Thermal Characteristics

Symbol	Parameter	FDA28N50F	Unit	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.4	°C/W	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient, Max.	40		

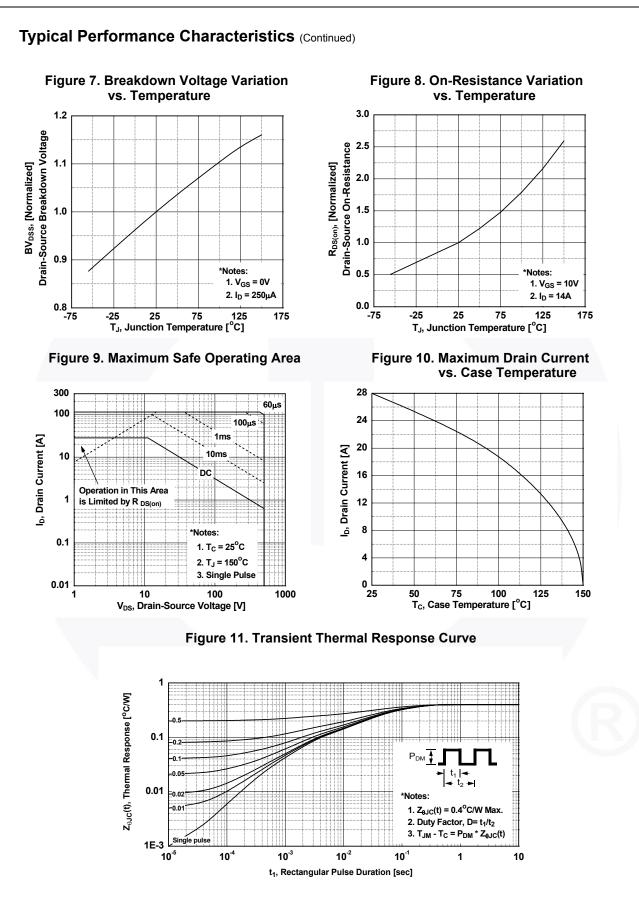
-		Top Mark	Package	Packing Method	Reel Size	e Ta	ape Width	Qua	antity
		TO-3PN	D-3PN Tube N/A		N/A		30 units		
Electrical	Chara	acteristics T _C = 25°C u	inless other	wise noted.					
Symbol		Parameter		Test Conditions	6	Min.	Тур.	Max.	Unit
Off Charact	teristics	i							
BV _{DSS}	Drain to Source Breakdown Voltage		lo =	I _D = 250 μA, V _{GS} = 0 V, T _J = 25 ^o C			-	-	V
∆BV _{DSS}	Breakdown Voltage Temperature Coefficient					500	_		-
ΔT_{J}			I _D =	$I_D = 250 \ \mu$ A, Referenced to 25° C			0.7	-	V/ºC
			V _{DS}	$V_{DS} = 500 V, V_{GS} = 0 V$ $V_{DS} = 400 V, T_{C} = 125^{\circ}C$		-	-	1	
DSS	Zero Gat	Zero Gate Voltage Drain Current				-	-	10	μA
I _{GSS}	Gate to Body Leakage Current		-	V _{GS} = ±30 V, V _{DS} = 0 V			-	±100	nA
On Charact	aristics								
V _{GS(th)}		reshold Voltage	Vee	_S = V _{DS} , I _D = 250 μA		3.0	_	5.0	V
R _{DS(on)}		ain to Source On Resistance		$s = 10 \text{ V}, I_D = 14 \text{ A}$		-	0.140	0.175	Ω
9FS		Transconductance		s = 20 V, I _D = 14 A		-	35	-	S
Dynamic C	haraata	riation							
•	Т						2075	5207	
C _{iss}		pacitance	V _{DS}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		-	3975	5387	pF
C _{oss}		apacitance Transfer Capacitance				-	566 38	753	pF pF
C _{rss}						-	80	56 105	nC
Q _{g(tot)}		te Charge at 10V Source Gate Charge		$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 28 \text{ A},$ $V_{GS} = 10 \text{ V}$		-	22		-
Q _{gs}		Drain "Miller" Charge	VGS			-	31	-	nC nC
Q _{gd}					(Note 4)	-	51	-	
Switching (Charact	eristics							
t _{d(on)}	Turn-On	Delay Time				-	67	145	ns
t _r	Turn-On	Rise Time		V_{DD} = 250 V, I _D = 28 A, V_{GS} = 10 V, R _G = 25 Ω		-	137	285	ns
t _{d(off)}	Turn-Off	Delay Time	VGS			-	192	395	ns
t _f	Turn-Off	Fall Time			(Note 4)	-	101	212	ns
Drain-Sour	ce Diod	e Characteristics							
s	Maximum Continuous Drain to Source Diode Forward Current					7-	-	28	Α
I _{SM}	Maximum	Pulsed Drain to Source Dio	de Forward				-	112	Α
V _{SD}		Source Diode Forward Voltag		s = 0 V, I _{SD} = 28 A		-	-	1.5	V
err (Reverse	Recovery Time		$V_{GS} = 0 V, I_{SD} = 28 A,$ $dI_F/dt = 100 A/\mu s$		-	266		ns
	Reverse	Recovery Charge				-	1.38	/ - · ·	μC

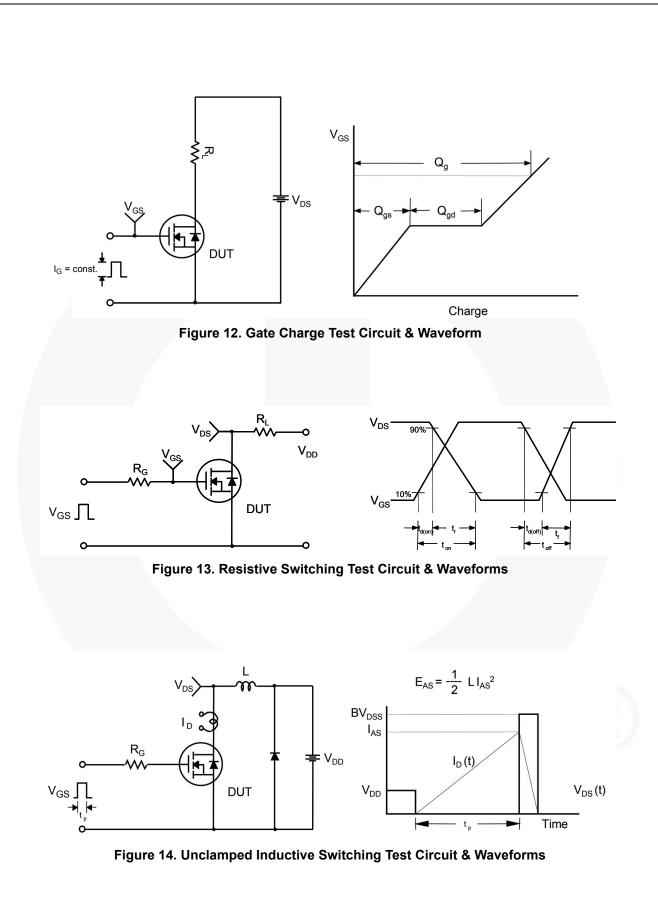
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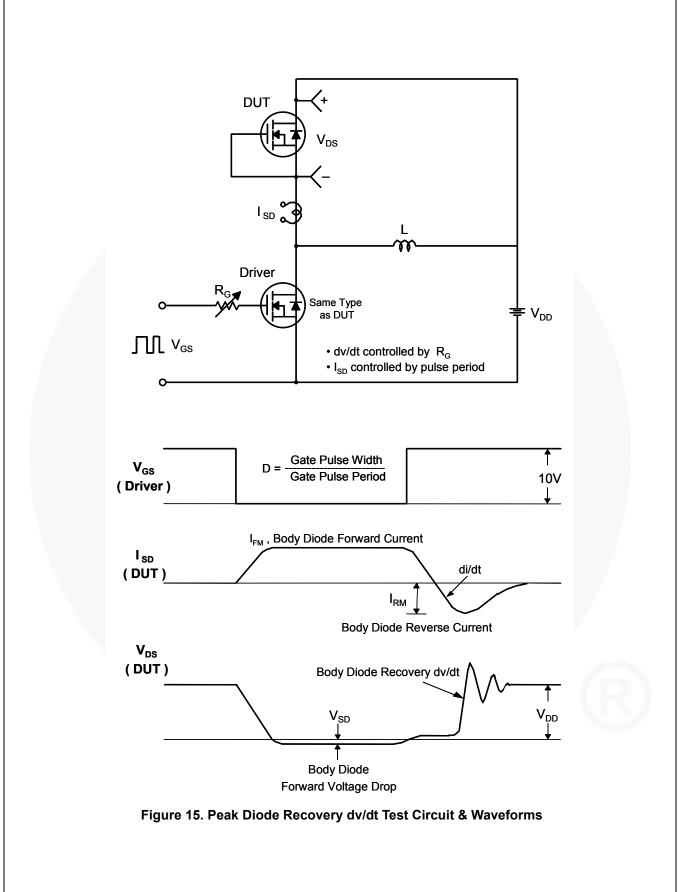


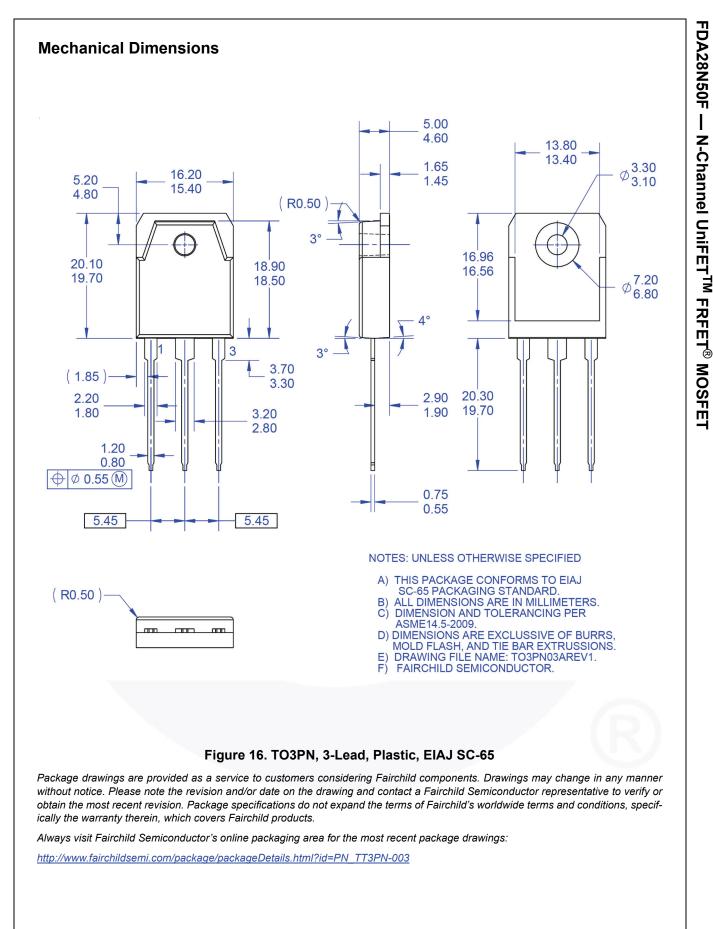


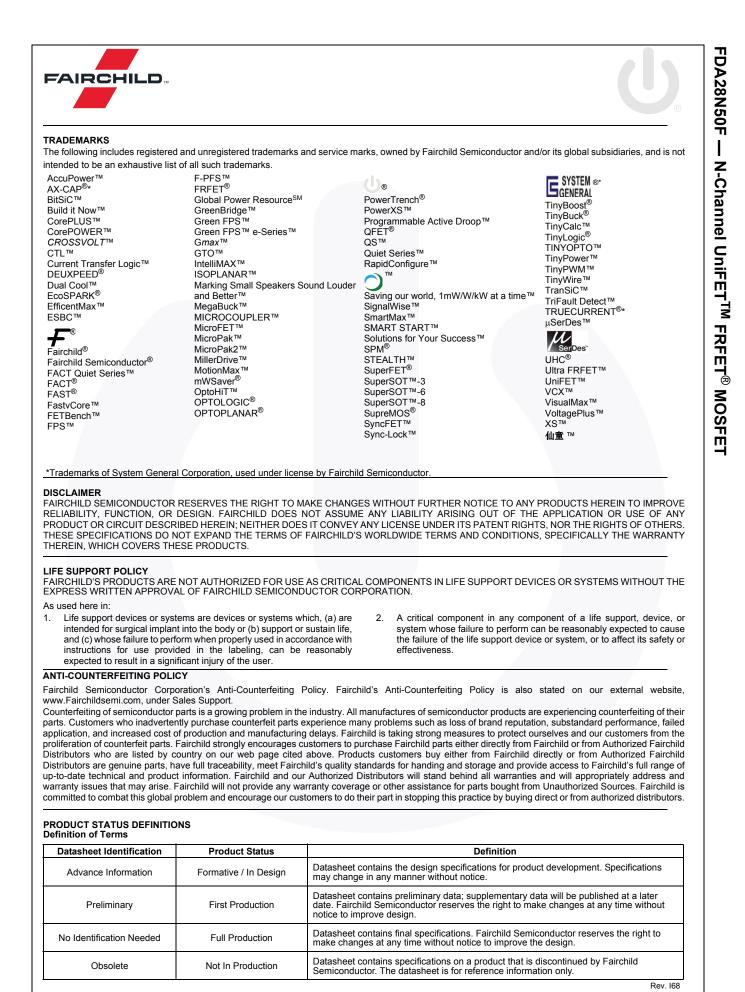
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