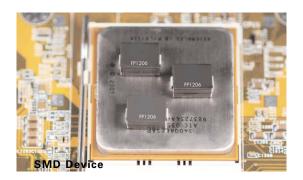
# FP1206

# High frequency, high current power inductors



#### **Product features**

- 8.0 x12.0 x 6.0mm surface mount package
- Ferrite core material
- High current carrying capacity, low core losses
- Designed for high speed, high current switch mode applications
- Controlled DCR tolerance for sensing circuits
- Inductance range from 120nH to 400nH
- Current range from 24 to 88 amps
- Frequency range up to 1MHz

#### **Applications**

- Multi-phase regulators
- Voltage Regulator Module (VRM)
- Desktop and server VRMs and EVRDs
- Data networking and storage systems
- Graphics cards and battery power systems
- Point of load modules
- · DCR current sensing

#### **Environmental data**

- Storage temperature range (component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature:
  J-STD-020 (latest revision) compliant







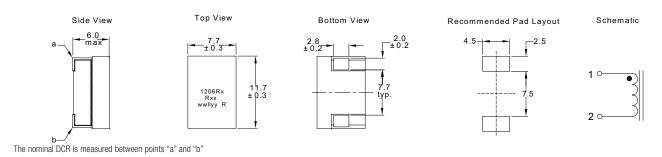


Product Specifications							
Part	OCL1	FLL <sup>2</sup>	I <sub>rms</sub> ³	l <sub>sat</sub> 1⁴	I <sub>sat</sub> 2⁵	DCR (mΩ)	
Number <sup>7</sup>	± 10% (nH)	Min. (nH)	(Amps)	(Amps) @25°C	(Amps) @125°C	@20°C	K-factor <sup>6</sup>
FP1206R1-R12-R	120	86		88	65		358
FP1206R1-R15-R	150	108		70	51		358
FP1206R1-R25-R	250	180	50	43	32	$0.43 \pm 6.5\%$	358
FP1206R1-R30-R	300	216		34	26		358
FP1206R1-R40-R	400	288		24	19		358

- 1 Open Circuit Inductance (OCL) Test Parameters:  $100 \mathrm{kHz}, 0.1 \mathrm{V}_{\mathrm{rms}}, 0.0 \mathrm{Adc}$
- 2 Full Load Inductance (FLL) Test Parameters: 100kHz, 0.1V<sub>rms</sub>, 0.0400 1 Full Load Inductance (FLL) Test Parameters: 100kHz, 0.1V<sub>rms</sub>, l<sub>sat</sub>1 2 I<sub>rms</sub>: DC current for an approximate temperature rise of 40°C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed 125°C under worst case operating conditions verified in the end application.
- 4 I<sub>sat</sub>1: Peak current for approximately 20% rolloff at +25°C.

- I<sub>Sat</sub>2: Peak current for approximately 20% rolloff at +125°C.
  K-factor: Used to determine B<sub>p-p</sub> for core loss (see graph). B<sub>p-p</sub> = K \* L \* ΔI \* 10³. B<sub>p-p</sub>:(Gauss), K: (K-factor from table), L: (Inductance in nH), ΔI (Peak-to-peak ripple current in amps).
  Part Number Definition: FP1206Rx-Rxx-R
- - FP1206 = Product code and size
  - Rx= DCR indicator
- Rxx= Inductance value in uH, R = decimal point
- -R suffix = RoHS compliant

#### **Dimensions- mm**



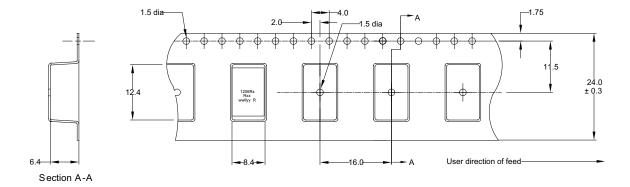
Part Marking: 1206Rx (Rx is the DCR indicator)

 $\mathsf{Rxx} = \mathsf{Inductance}\ \mathsf{value}\ \mathsf{in}\ \mu\mathsf{H}.\ (\mathsf{R} = \mathsf{Decimal}\ \mathsf{point}).$ 

wwllyy = Date code

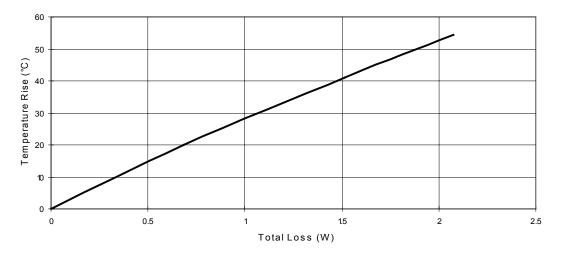
R = Revision level

#### Packaging information - mm

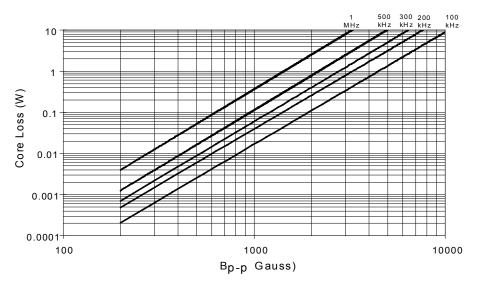


Supplied in tape-and-reel packaging, 620 parts per reel, 13" diameter reel.

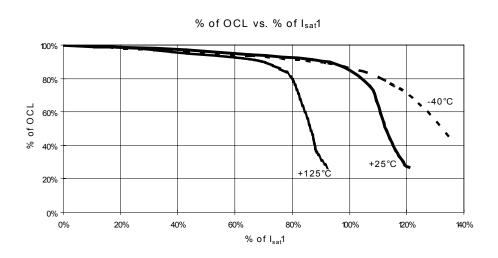
# Temperature rise vs total loss



## Core loss vs Bp-p



## **Inductance characteristics**



#### **Solder Reflow Profile**

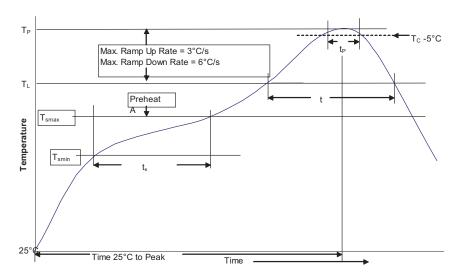


Table 1 - Standard SnPb Solder (T<sub>c</sub>)

		Volume	Volume
Pag	ckage	mm³	mm³
Th	nickness	<350	≥350
<	2.5mm	235°C	220°C
_≥′	2.5mm	220°C	220°C

Table 2 - Lead (Pb) Free Solder (Tc)

	Volume	Volume	Volume
Package	mm³	mm³	mm <sup>3</sup>
Thickness	<350	350 - 2000	>2000
<1.6mm	260°C	260°C	260°C
1.6 - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

#### **Reference JDEC J-STD-020**

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak	• Temperature min. (T <sub>smin</sub> )	100°C	150°C
	Temperature max. (T <sub>smax</sub> )	150°C	200°C
	• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds
Average ramp up ra	te T <sub>smax</sub> to T <sub>p</sub>	3°C/ Second Max.	3°C/ Second Max.
Liquidous temperature (TL)		183°C	217°C
Time at liquidous (t <sub>L</sub> )		60-150 Seconds	60-150 Seconds
Peak package body temperature (Tp)*		Table 1	Table 2
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**
Average ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )		6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.

 $<sup>^{\</sup>star}$  Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

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<sup>\*\*</sup> Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.