

Vishay Semiconductors

Small Signal Switching Diode, High Voltage

FEATURES

- Silicon epitaxial planar diode
- Fast switching diode, especially suited for applications requiring high voltage capability
- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade RoHS
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



MECHANICAL DATA

Case: SOD-323 Weight: approx. 4.3 mg Packaging codes / options: 18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE						
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS		
GSD2004WS	GSD2004WS-E3-08 or GSD2004WS-E3-18	Single	B6	Tape and reel		
	GSD2004WS-HE3-08 or GSD2004WS-HE3-18	Single	БО			

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		V _R	240	V	
Repetitive peak reverse voltage		V _{RRM}	300	V	
Forward current (continuous)		I _F	225	mA	
Peak repetitive forward current		I _{FRM}	625	mA	
Non repetitive peak ferward aurrent	t _p = 1 μs	I _{FSM}	4	A	
Non-repetitive peak forward current	t _p = 1 s	I _{FSM}	1	А	
Power dissipation ⁽¹⁾		P _{tot}	200	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Typical thermal resistance junction to ambient air ⁽¹⁾		R _{thJA}	650	K/W
Junction temperature		Tj	150	°C
Storage temperature range		T _{stg}	-65 to +150	°C
Operating temperature range		T _{op}	-55 to +150	°C

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I _R = 100 μA	V _{BR}	300			V
Lackage everent	V _R = 240 V	I _R			100	nA
Leakage current	V _R = 240 V, T _j = 150 °C	I _R			100	μA
Forward voltage	I _F = 20 mA	V _F		0.83	0.87	V
	I _F = 100 mA	VF			1	V
Diode capacitance	$V_F = V_R = 0$, f = 1 MHz	CD			5	pF
Reverse recovery time	$I_{F} = I_{R} = 30 \text{ mA}, i_{R} = 3 \text{ mA}, \\ R_{L} = 100 \Omega$	t _{rr}			50	ns

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

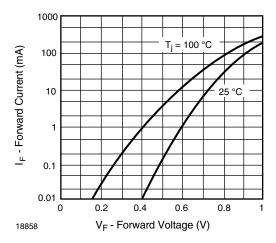


Fig. 1 - Forward Current vs. Forward Voltage

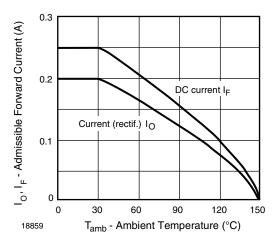


Fig. 2 - Admissible Forward Current vs. Ambient Temperature

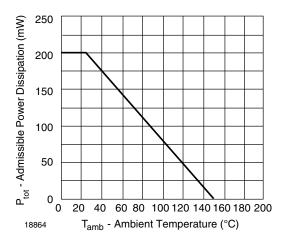


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

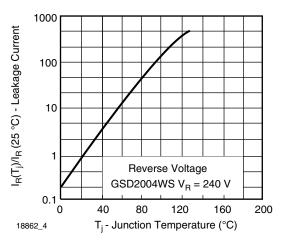


Fig. 4 - Leakage Current vs. Junction Temperature

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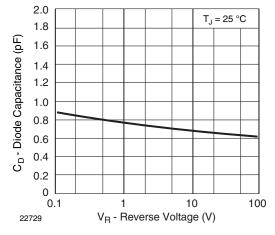
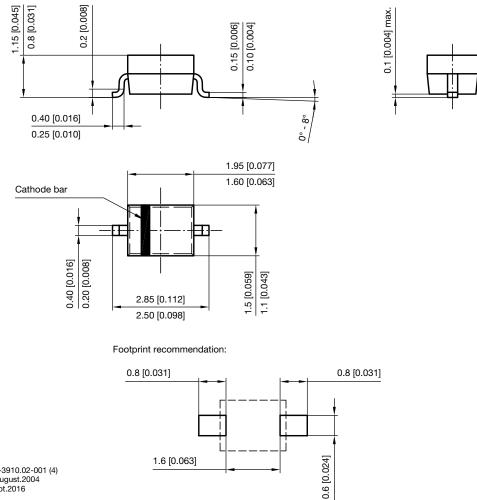


Fig. 5 - Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-323



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