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February 2015

# **KSA916 PNP Epitaxial Silicon Transistor**

## **Features**

- Audio Power Amplifier
- · Driver Stage Amplifier
- · Complement to KSC2316



1. Emitter 2. Collector 3. Base

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
KSA916YTA	A916	TO-92 3L	Ammo

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	-120	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-120	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
I <sub>C</sub>	Collector Current	-800	mA
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to +150	°C

## Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
	Power Dissipation, by R <sub>θJA</sub>	900	mW
D	Power Dissipation, by R <sub>0JC</sub>	3	W
P <sub>D</sub>	Derate Above 25°C, by R <sub>θJA</sub>	7.2	mW/°C
	Derate Above 25°C, by R <sub>0JC</sub>	24	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	130	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	41	°C/W

## Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -1 \text{ mA}, I_E = 0$	-120			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-120	\		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -1 \text{ mA}, I_C = 0$	-5			V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = -120 V, I <sub>E</sub> = 0			-0.1	μΑ
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	60			
h <sub>FE2</sub>	DC Current Gain	$V_{CE} = -5 \text{ V}, I_{C} = -100 \text{ mA}$	80		240	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$			-1	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -5V, I_{C} = -100 \text{ mA}$		120		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1 MHz			40	pF

## h<sub>FE</sub> Classification

Classification	0	Y
h <sub>FE2</sub>	80 ~ 160	120 ~ 240

## **Typical Performance Characteristics**

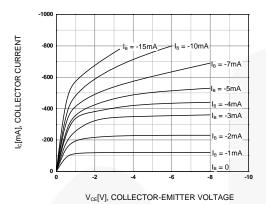


Figure 1. Static Characteristic

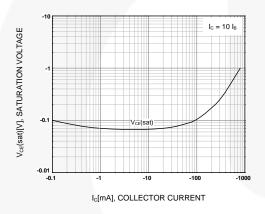


Figure 3. Collector-Emitter Saturation Voltage

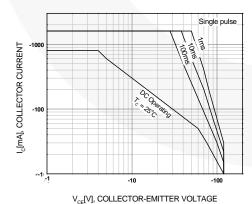


Figure 5. Safe Operating Area

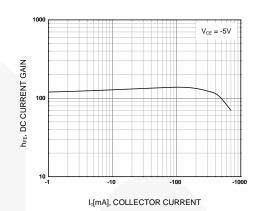


Figure 2. DC Current Gain

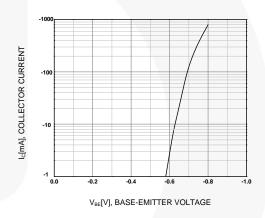


Figure 4. Base-Emitter On Voltage

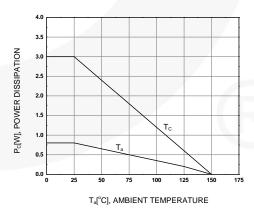


Figure 6. Power Derating

## **Physical Dimensions**

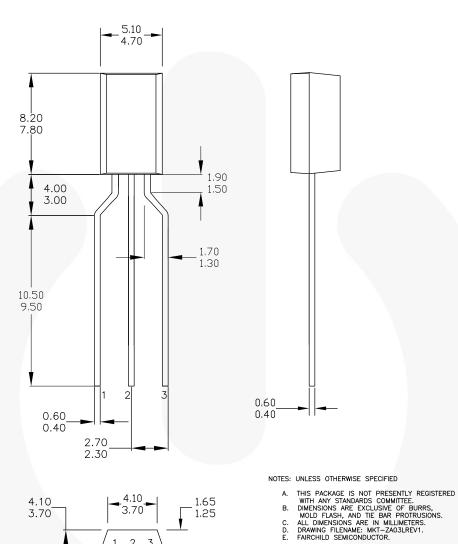


Figure 7. 3-LEAD, TO-92L, NON-JEDEC 8 MM TALL BODY LEAD FORM TA TYPE

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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