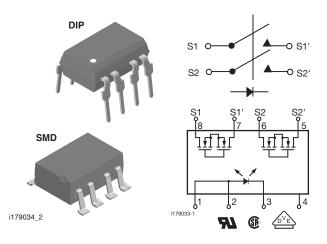


**Vishay Semiconductors** 

# 2 Form A Solid-State Relay



### DESCRIPTION

The LH1503 relays are DPST normally open switches (2 form A) that can replace electromechanical relays in many applications. The relays are constructed using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high voltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuity, and DMOS switches. In addition, these relays employ current limiting circuity, enabling them to pass lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

### **FEATURES**

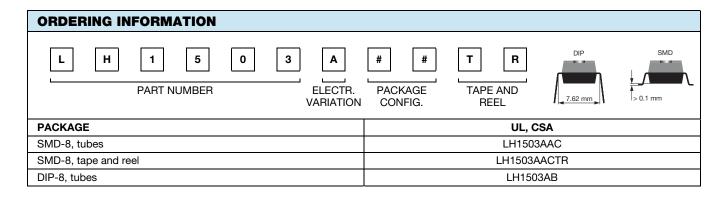
- · Current limit protection
- Isolation test voltage 5300 V<sub>BMS</sub>
- Typical R<sub>ON</sub> 20 Ω
- Load voltage 350 V
- Load current 110 mA
- High surge capability
- · Clean bounce free switching
- Low power consumption
- High reliability monolithic receptor
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### APPLICATIONS

- General telecom switching
  - On/off hook control
  - Ring delay
  - Dial pulse
  - Ground start
  - Ground fault protection
- Instrumentation
- Industrial controls

### AGENCY APPROVALS

- UL1577: file no. E52744 system code H, double protection CSA: certification no. 093751
- DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending), available with option 1



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ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT	
INPUT					
LED continuous forward current		I <sub>F</sub>	50	mA	
LED reverse voltage	I <sub>R</sub> ≤ 10 μA	V <sub>R</sub>	8	V	
OUTPUT					
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	350	V	
Continuous DC load current one pole operating		١L	150	mA	
Continuous DC load current two poles operating		ΙL	110	mA	
Peak load current (single shot)	t = 100 ms	l <sub>P</sub>	(1)		
SSR					
Ambient temperature range		T <sub>amb</sub>	- 40 to + 85	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 150	°C	
Pin soldering temperature <sup>(2)</sup>	t = 10 s max.	T <sub>sld</sub>	260	°C	
Input to output isolation voltage		V <sub>ISO</sub>	5300	V <sub>RMS</sub>	
Pole-to-pole isolation voltage (S1 to S2)			500	V	
Output power dissipation (continuous)		P <sub>diss</sub>	600	mW	

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

(1) Refer to current limit performance application note for a discussion on relay operation during transient currents.

<sup>(2)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT		•				•
LED forward current, switch turn-on	l <sub>L</sub> = 100 mA, t = 10 ms	I <sub>Fon</sub>		2	3	mA
LED forward current, switch turn-off	$V_L = \pm 300 V$	I <sub>Foff</sub>	0.2	0.8		mA
LED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.15	1.26	1.45	V
OUTPUT						
On-resistance	I <sub>F</sub> = 5 mA, I <sub>L</sub> = 50 mA	R <sub>ON</sub>	12	20	25	Ω
Pole-to-pole on-resistance matching (S1 to S2)	I <sub>F</sub> = 5 mA, I <sub>L</sub> = 50 mA			0.2	2	ΔΩ
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R <sub>OFF</sub>	0.5	5000		GΩ
Current limit	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	I <sub>LMT</sub>	230	270	370	mA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι <sub>Ο</sub>		0.02	200	nA
	$I_F = 0 \text{ mA}, V_L = \pm 350 \text{ V}$	Ι <sub>Ο</sub>			1	μA
	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}$	Co		55		pF
Output capacitance	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co		10		pF
Pole-to-pole capacitance (S1 to S2)	$I_F = 0 \text{ mA}$			3		pF
	$I_F = 5 \text{ mA}$			4		pF
Switch offset	$I_F = 5 \text{ mA}$	V <sub>OS</sub>		0.15		μV
TRANSFER					•	
Capacitance (input to output)	V <sub>ISO</sub>	C <sub>ISO</sub>		1.1		pF

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I <sub>F</sub> = 10 mA, I <sub>L</sub> = 50 mA	t <sub>on</sub>		1.6	2.5	ms
Turn-off time	$I_{F} = 10 \text{ mA}, I_{L} = 50 \text{ mA}$	t <sub>off</sub>		0.65	2.5	ms

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### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

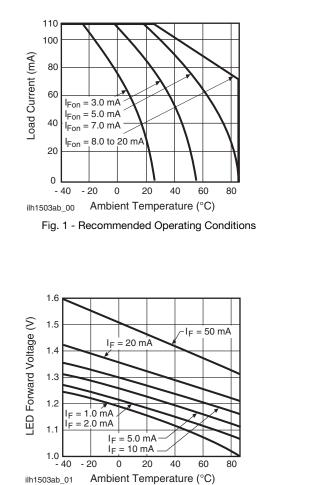


Fig. 2 - LED Voltage vs. Temperature

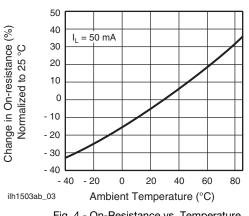
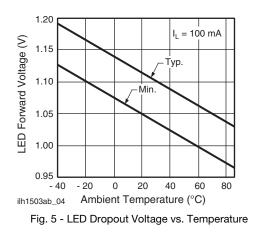
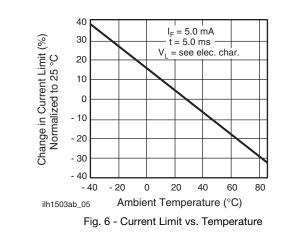


Fig. 4 - On-Resistance vs. Temperature





Furn-on (%), Normalized to 25  $^\circ\text{C}$ -ED Forward Current for Switch 100 = 100 mA 80 60 40 20 0 - 20 - 40 - 60 80 40 - 20 0 20 40 60 Ambient Temperature (°C) ilh1503ab\_02

Fig. 3 - LED Current for Switch Turn-on vs. Temperature

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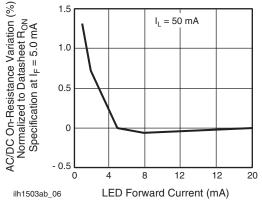


Fig. 7 - Variation in On-Resistance vs. LED Current

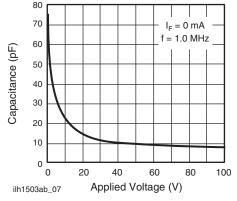
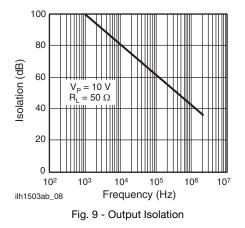


Fig. 8 - Switch Capacitance vs. Applied Voltage



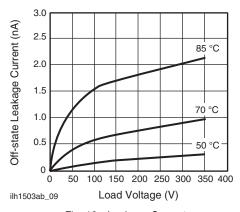
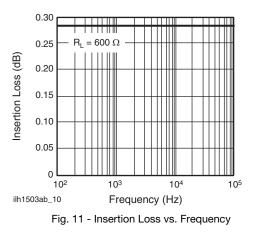
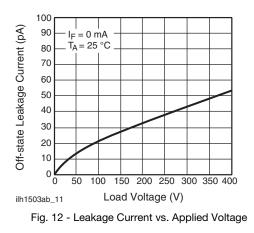


Fig. 10 - Leakage Current vs. Applied Voltage at Elevated Temperatures





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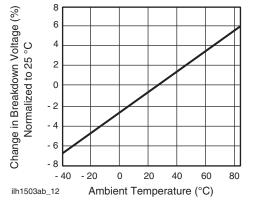


Fig. 13 - Switch Breakdown Voltage vs. Temperature

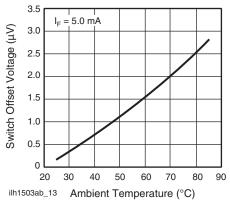


Fig. 14 - Switch Offset Voltage vs. Temperature

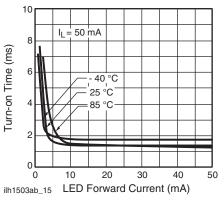


Fig. 16 - Turn-on Time vs. LED Current

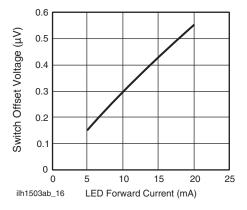
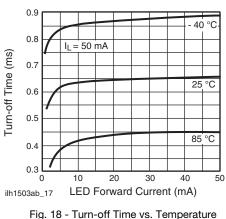


Fig. 17 - Switch Offset Voltage vs. LED Current



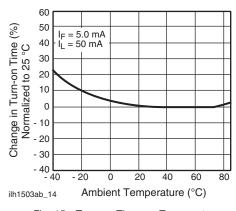


Fig. 15 - Turn-on Time vs. Temperature

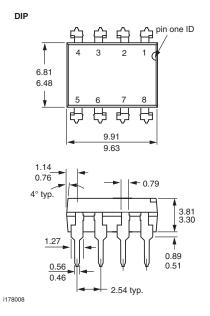
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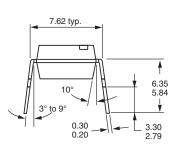
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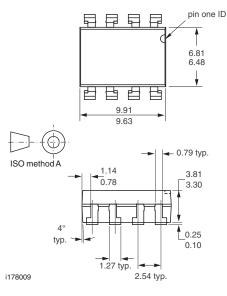
### **PACKAGE DIMENSIONS** in millimeters

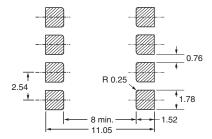


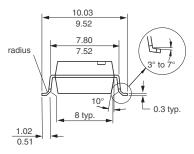


ISO method A

SMD







### PACKAGE MARKING (example)



### Note

• Tape and reel suffix (TR) is not part of the package marking.

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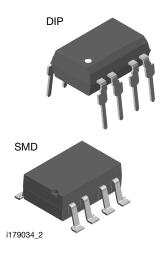
## Footprint and Schematic Information for LH1503AAC, LH1503AACTR, LH1503AB

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC	
LH1503AAC	www.snapeda.com/parts/LH1503AAC/Vishay/view-part	
LH1503AACTR	www.snapeda.com/parts/LH1503AACTR/Vishay/view-part	
LH1503AB	www.snapeda.com/parts/LH1503AB/Vishay/view-part	

For technical issues and product support, please contact optocoupleranswers@vishay.com.





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