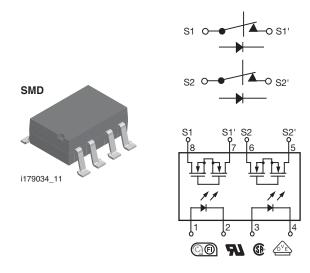


Vishay Semiconductors

Dual 1 Form B Solid-State Relay



DESCRIPTION

The LH1521 dual 1 form B relays are SPST normally closed switches that can replace electromechanical relays in many applications. The relays are constructed as a multi chip hybrid device. Actuation control is via an infrared LED. The output switch is a combination of a photodiode array with MOSFET switches and control circuity.

FEATURES

- Dual channel (LH1501)
- Isolation test voltage 3750 V_{RMS}
- Typical R_{ON} 20 Ω
- Load voltage 350 V
- Load current 150 mA
- · High surge capability
- · Clean bounce free switching
- Low power consumption
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- · General telecom switching
 - On/off hook control
 - Ring delay
 - Dial pulse
 - Ground start
 - Ground fault protection
- Instrumentation
- Industrial controls

AGENCY APPROVALS

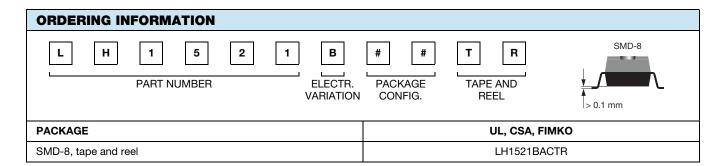
UL1577: file no. E52744 system code H, double protection

CSA: certification no. 093751

DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending),

available with option 1

FIMKO: 25419





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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
LED continuous forward current		I _F	50	mA	
LED reverse voltage	I _R ≤ 10 μA	V _R	5	V	
OUTPUT					
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	V _L 350		V	
Continuous DC load current one pole operating		ΙL	150	mA	
Continuous DC load current two poles operating		lL	110	mA	
Peak load current (single shot)	t = 100 ms	I _P	400	mA	
SSR					
Output power dissipation (continuous)		P _{diss}	600	mW	
Ambient temperature range		T _{amb}	- 40 to + 85	°C	
Storage temperature range		T _{stg}	- 40 to + 125	°C	
Pin soldering temperature (2)	t = 10 s max.	T _{sld}	260	°C	
Input to output isolation voltage	$t = 1 \text{ s, } I_{ISO} = 10 \mu\text{A max.}$	V _{ISO}	3750	V _{RMS}	
Pole-to-pole isolation voltage (S1 to S2) (1) (dry air, dust free, at sea level)			1600	V	

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Breakdown occurs between the output pins external to the package.
- (2) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED forward current switch turn-on	$I_L = \pm 150 \text{ mA}, t = 10 \text{ ms}$	I _{Fon}	0.2	0.9		mA
LED forward current switch turn-off	$V_{L} = \pm 300 \text{ V}$	I _{Foff}		1	2	mA
LED forward voltage	I _F = 10 mA	V _F	1.15	1.22	1.45	V
OUTPUT						
On-resistance	$I_F = 0 \text{ mA}, I_L = 50 \text{ mA}$	R _{ON}		20	25	Ω
Off-resistance	$I_F = 5 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.1	1.4		GΩ
Off-state leakage current	$I_F = 5 \text{ mA}, V_L = \pm 350 \text{ V}$			0.08	1	μΑ
Output capacitance	$I_F = 5 \text{ mA}, V_L = 50 \text{ V}$			50		pF
TRANSFER						
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}		3		pF

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}		2	3	ms
Turn-off time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}		1	3	ms

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

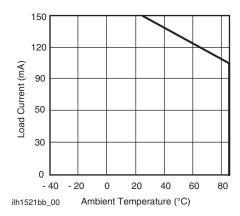
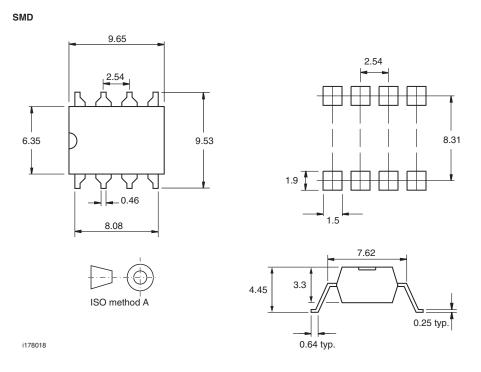
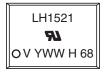


Fig. 1 - Recommended Operating Conditions

PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)



Note

• Tape and reel suffix (TR) is not part of the package marking.



Footprint and Schematic Information

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Footprint and Schematic Information for LH1521BACTR

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
LH1521BACTR	www.snapeda.com/parts/LH1521BACTR/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishay.com.





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