

Digital Attenuator, 10 dB, 1-Bit DC - 2.0 GHz

Rev. V3

Features

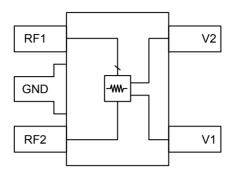
- Single 10 dB Step
- Low Loss: 0.3 dB @ 900 MHz
- Lead-Free SOT-25 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of AT-266

Description

The MAATSS0018 is a 1 bit, 10 dB step GaAs MMIC digital attenuator in a lead-free SOT-25 surface mount plastic package. This device uses a mature 1-micron process. The process features full chip passivation for increased performance and reliability.

The MAATSS0018 is ideally suited for use where high accuracy, very low power consumption and low intermodulation products are required. Typical applications include radio, wireless LANs, GPS equipment and other gain / level control circuits.

Functional Block Diagram



Pin Configuration

Pin No.	Function	Description		
1	RF1	RF In/Out		
2	GND	RF Ground		
3	RF2	RF In/Out		
4	V1	Control Voltage		
5	V2	Control Voltage		

Ordering Information^{1,2}

Part Number	Package
MAATSS0018TR-3000	3000 piece reel
MAATSS0018SMB	Sample Board

- 1. Reference Application Note M513 for reel size information.
- 2. All sample boards include 5 loose parts.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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^{*} Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.



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Electrical Specifications: $T_A = +25^{\circ}C$, $V_C = 0 \text{ V}$ / -3 V, $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	0 - 1 GHz 1 - 2 GHz	dB	_	0.3 0.5	0.45 0.7
Attenuation	DC - 2 GHz	dB	9.5	10	10.5
VSWR	0 - 2 GHz	Ratio	_	1.4:1	1
IP ₃	2 Tone @ 0 dBm, 5 MHz spacing	dBm	_	50	_
P1dB	1 GHz	dBm	_	28	_
T _{RISE} , T _{FALL}	10% to 90% RF, 90% to 10% RF	ns	_	5	_
T _{ON} , T _{OFF}	50% Control to 90% RF, 50% Control to 10% RF	ns	_	10	_
Transients	In Band	mV	_	6	_
Control Current	V _C = 3 V	μA	_	25	

Absolute Maximum Ratings^{3,4}

Parameter	Absolute Maximum		
Input Power 50 MHz 500 - 2000 MHz	+27 dBm +34 dBm		
Control Voltage	-8.5 V ≤ V _C ≤ +8 V		
Operating Temperature	-40°C to +85°C		
Storing Temperature	-65°C to +150°C		

^{3.} Exceeding any one or combination of these limits may cause permanent damage to this device.

Truth Table 5,6,7

V1	V2	Attenuation State
0	1	10 dB
1	0	Insertion Loss

- For positive voltage control, external DC blocking capacitors are required on all RF ports (pins 1, 2 and 3)
- Differential voltage, V(state 1) V(state 0), must be +2.8 V minimum and less than 8 V.
- 7. 0 = -8 V to 0.2 V, 1 = -0.2 V to 8 V

MACOM does not recommend sustained operation near these survivability limits.

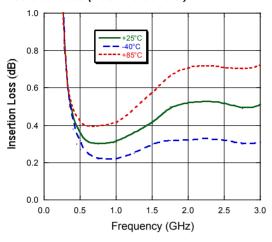


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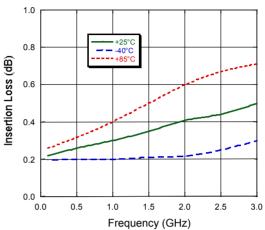
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Typical Performance Curves (39 pF capacitors used for positive voltage control)

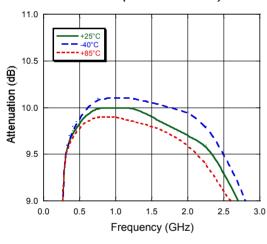
Insertion Loss (Positive Control)



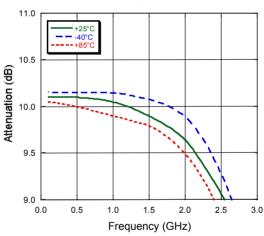
Insertion Loss (Negative Control)



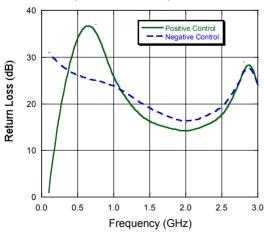
Relative Attenuation (Positive Control)



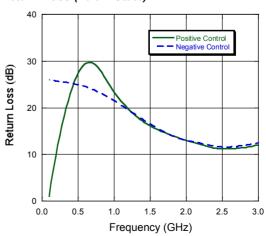
Relative Attenuation (Negative Control)



Return Loss (Reference State)



Return Loss (10 dB State)



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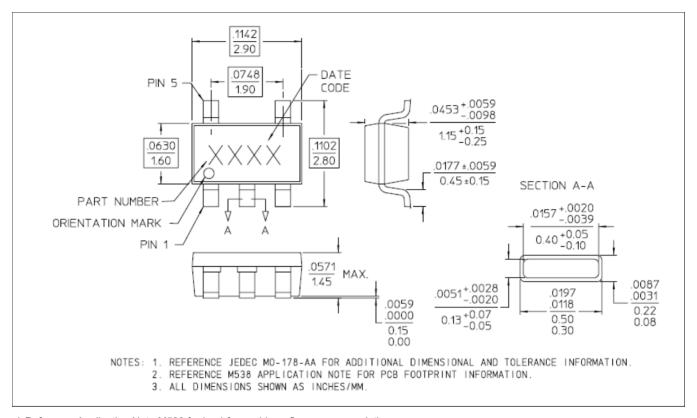
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Lead-Free SOT-25[†]



† Reference Application Note M538 for lead-free solder reflow recommendations.



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