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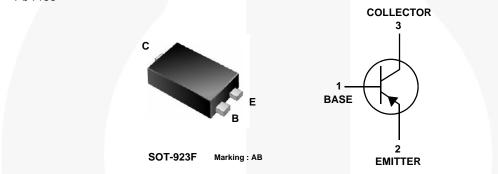
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## MMBT3906SL PNP Epitaxial Silicon Transistor

## Features

- General-Purpose Amplifier Transistor
- Ultra Small Surface Mount Package for All Types (Max. 0.43mm Tall)
- · Suitable for General Switching and Amplification
- Well Suited for Portable Application
- As Complementary type, NPN MMBT3904SL is Recommended.
- Pb Free



#### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MMBT3906SL	AB	SOT-923F 3L	Tape and Reel

#### Absolute Maximum Ratings<sup>(1)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	-40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
۱ <sub>C</sub>	Collector Current	-200	mA
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

Note:

1. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

June 2015

## **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
Б	Power Dissipation, by $R_{\theta JA}$	227	mW
PD	Derate Above 25°C	1.81	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient <sup>(2)</sup>	550	°C/W

Note:

2. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

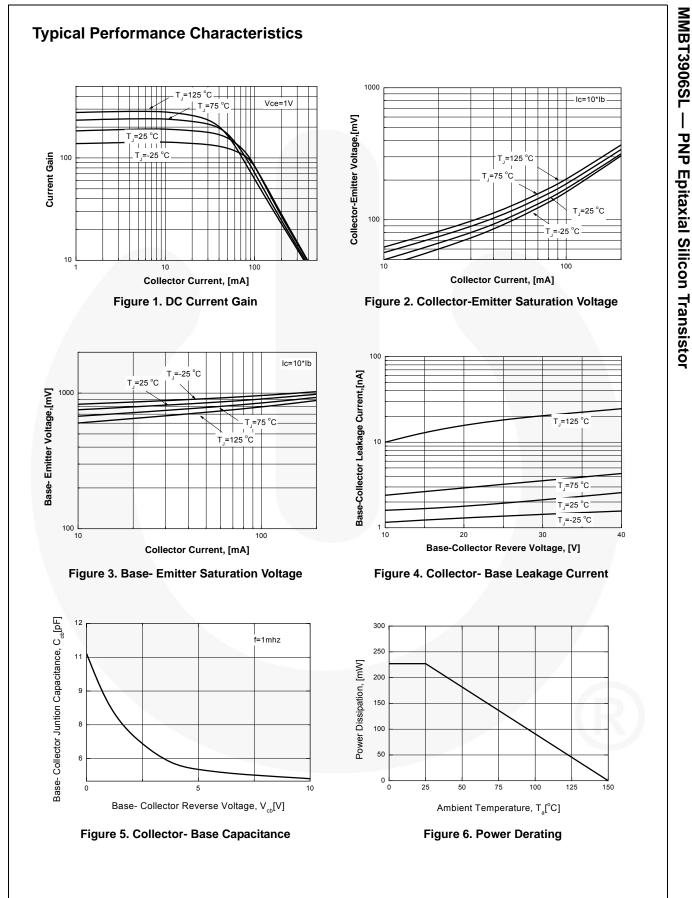
### **Electrical Characteristics**<sup>(3)</sup>

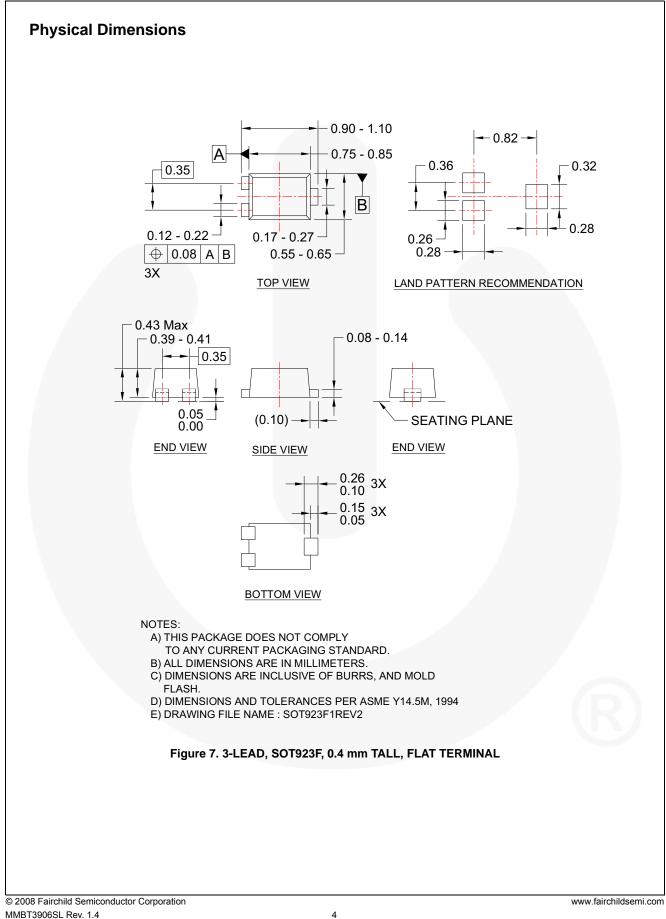
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -10 μA, I <sub>E</sub> = 0	-40		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1 mA, I <sub>B</sub> = 0	-40		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -10 μA, I <sub>C</sub> = 0	-5		V
I <sub>CEX</sub>	Collector Cut-Off Current	$V_{CE}$ = -30 V, $V_{EB(OFF)}$ = -0.3 V		-50	nA
		V <sub>CE</sub> = -1.0 V, I <sub>C</sub> = -0.1 mA	60		
		V <sub>CE</sub> = -1.0 V, I <sub>C</sub> = -1 mA	80		
h <sub>FE</sub>	DC Current Gain	V <sub>CE</sub> = -1.0 V, I <sub>C</sub> = -10 mA	100	300	
		V <sub>CE</sub> = -1.0 V, I <sub>C</sub> = -50 mA	60		
		V <sub>CE</sub> = -1.0 V, I <sub>C</sub> = -100 mA	30		
M	Collector Emitter Seturation Voltage	I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1.0 mA		-0.25	- V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5.0 mA		-0.40	
V	Base-Emitter Saturation Voltage	I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1.0 mA	-0.65	-0.85	- V
V <sub>BE(sat)</sub>	Base-Emilier Saturation voltage	I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5.0 mA		-0.95	
f <sub>T</sub>	Current Gain-Bandwidth Product	V <sub>CE</sub> = -20 V, I <sub>C</sub> = -10 mA, f = 100 MHz	250		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -5 V, I <sub>E</sub> = 0, f = 1 MHz		7.0	pF
C <sub>ib</sub>	Input Capacitance	V <sub>EB</sub> = -0.5 V, I <sub>C</sub> = 0, f = 1 MHz		15	pF
t <sub>d</sub>	Delay Time			35	ns
t <sub>r</sub>	Rise Time	V <sub>CC</sub> = -3 V, I <sub>C</sub> = -10 mA,		35	ns
t <sub>s</sub>	Storage Time	$I_{B1} = -I_{B2} = -1 \text{ mA}$		225	ns
t <sub>f</sub>	Fall Time		1	75	ns

#### Note:

3. DC Item are tested by pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%.





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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.
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