

TinyLogic UHS Triple Inverter

NC7NZ04

Desription

The NC7NZ04 is a triple inverter from **onsemi**'s Ultra–High Speed (UHS) series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra–high speed with high output drive while maintaining low static power dissipation over a broad V_{CC} operating range. The device is specified to operate over the 1.65 V to 5.5 V V_{CC} operating range. The inputs and output are high impedance when V_{CC} is 0 V. Inputs tolerate voltages up to 5.5 V, independent of V_{CC} operating voltage.

Features

- Ultra-High Speed: t_{PD} = 2.4 ns (Typical) into 50 pF at 5 V V_{CC}
- High Output Drive: ±24 mA at 3 V V_{CC}
- Broad V_{CC} Operating Range: 1.65 V to 5.5 V Power–Down, High–Impedance Inputs / Outputs
- Over-Voltage Tolerance Inputs Facilitate 5 V to 3 V Translation
- Proprietary Noise / EMI Reduction Circuitry
- Space–Saving MicroPak[™] and US8 Surface Mount Packages
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

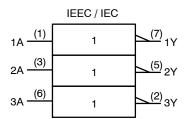


Figure 1. Logic Symbol

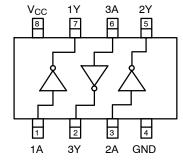


Figure 2. Connection Diagram

MARKING DIAGRAMS



US8 CASE 846AN





JQFN8 1.6X1.6, 0.5P CASE 523AY



NZ04, T3 = Specific Device Code A = Assembly Site L = Wafer Lot Number YW = Assembly Start Week

KK = 2-Digit Lot Run Traceability Code

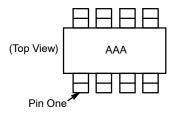
XY = 2-Digit Date Code Z = Assembly Plant Code

ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 5 of this data sheet.

1

Pin Configurations



NOTES:

- 1. AAA represents product code top mark (see ordering table).
- Orientation of top mark determines pin one location. Reading the top product code mark left to right, pin one is the lower left pin.

Figure 3. US8

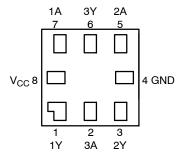


Figure 4. MicroPak (Top Through View)

PIN DEFINITIONS

Pin # US8	Pin # MicroPak	Name	Description
1	7	1A	Input
2	6	3Y	Output
3	5	2A	Input
4	4	GND	Ground
5	3	2Y	Output
6	2	ЗА	Input
7	1	1Y	Output
8	8	V _{CC}	Supply Voltage

FUNCTION TABLE

Inputs	Output
Α	Y
L	Н
Н	L

ABSOLUTE MAXIMUM RATINGS

Symbol	Paramete	Min	Max	Unit	
V_{CC}	Supply Voltage		-0.5	6.5	V
V _{IN}	DC Input Voltage		-0.5	6.5	V
V _{OUT}	DC Output Voltage		-0.5	6.5	V
I _{IK}	DC Input Diode Current	V _{IN} < 0 V	-	-50	mA
l _{ok}	DC Output Diode Current	V _{OUT} < 0 V	-	-50	mA
l _{OUT}	DC Output Current	-	±50	mA	
I _{CC} or I _{GND}	DC V _{CC} or Ground Current		-	±50	mA
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under Bias		-	+150	°C
TL	Junction Lead Temperature (Solderin	g, 10 Seconds)	-	+260	°C
P_{D}	Power Dissipation in Still Air	US8	-	500	mW
		MicroPak-8	-	539	
ESD	Human Body Model, JEDEC: JESD22-A114 Charge Device Model, JEDEC: JESD22-C101		-	4000	V
			-	2000	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

NC7NZ04

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage Operating		1.65	5.5	V
	Supply Voltage Data Retention		1.5	5.5	
V _{IN}	Input Voltage		0	5.5	V
V _{OUT}	Output Voltage		0	V _{CC}	V
T _A	Operating Temperature		-40	+85	°C
t _r , t _f	Input Rise and Fall Times	V _{CC} at 1.8 V, 2.5 V ±0.2 V	0	20	ns/V
		V_{CC} at 3.3 V ± 0.3 V	0	10	
		V_{CC} at 5.0 V ± 0.5 V	0	5	
$\theta_{\sf JA}$	Thermal Resistance	US8	-	250	°C/W
		MicroPak-8	=	232	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

3. Unused inputs must be held HIGH or LOW. They may not float.

DC ELECTICAL CHARACTERISTICS

				T _A = 25°C		С	$T_A = -40$	to 85°C	
Symbol	Parameter	V _{CC}	Conditions	Min	Тур	Max	Min	Max	Unit
V _{IH}	HIGH Level Input Voltage	1.65 to 1.95		0.65 V _{CC}	_	-	0.65 V _{CC}	-	V
		2.30 to 5.50		0.70 V _{CC}	_	-	0.70 V _{CC}	-	
V_{IL}	LOW Level Input Voltage	1.65 to 1.95		-	_	0.35 V _{CC}	-	0.35 V _{CC}	V
		2.30 to 5.50		-	_	0.30 V _{CC}	-	0.30 V _{CC}	
V _{OH}	HIGH Level Output	1.65	$V_{IN} = V_{IH} \text{ or } V_{IL},$	1.55	1.65	-	1.55	-	V
	Voltage	2.30	I _{OH} = -100 μA	2.20	2.30	-	2.20	-	
		3.00		2.90	3.00	-	2.90	-	
		4.50		4.40	4.50	-	4.40	-	
		1.65	I _{OH} = -4 mA	1.29	1.52	-	1.29	-	
		2.30	I _{OH} = -8 mA	1.90	2.15	-	1.90	-	
		3.00	I _{OH} = -16 mA	2.40	2.80	-	2.40	-	
		3.00	I _{OH} = -24 mA	2.30	2.68	-	2.30	-	
		4.50	I _{OH} = -32 mA	3.80	4.20	-	3.80	-	
V _{OL}	LOW Level Output	1.65	$V_{IN} = V_{IH} \text{ or } V_{IL},$	-	0.00	0.10	=	0.10	V
	Voltage	2.30	I _{OH} = -100 μA	-	0.00	0.10	_	0.10	
		3.00		-	0.00	0.10	_	0.10	
		4.50		-	0.00	0.10	_	0.10	
		1.65	I _{OH} = 4 mA	-	0.80	0.24	_	0.24	
		2.30	I _{OH} = 8 mA	-	0.10	0.30	_	0.30	
		3.00	I _{OH} = 16 mA	-	0.15	0.40	_	0.40	
		3.00	I _{OH} = 24 mA	-	0.22	0.55	_	0.55	
		4.50	I _{OH} = 32 mA	-	0.22	0.55	-	0.55	
I _{IN}	Input Leakage Current	1.65 to 5.5	V _{IN} = 5.5 V, GND	-	_	±1	_	±1	μΑ
l _{OFF}	Power-Off Leakage Current	0	V _{IN} or V _{OUT} = 5.5 V	-	-	1	-	10	μΑ
I _{CC}	Quiescent Supply Current	1.65 to 5.50	V _{IN} = 5.5 V, GND	-	-	1	-	10	μΑ

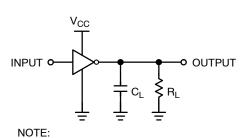
AC ELECTRICAL CHARACTERISTICS

				T _A = 25°C			$T_A = -40$	to 85°C	
Symbol	Parameter	V _{CC}	Conditions	Min	Тур	Max	Min	Max	Unit
t _{PLH} , t _{PHL}		1.80 ±0.15	C _L = 15 pF,	=	4.4	9.5	-	10.0	ns
	(Figure 5, 6)	2.50 ±0.20	$R_L = 1 M\Omega$	=	2.9	5.1	-	5.6	
		3.30 ±0.30		=	2.1	3.4	-	3.8	
		5.00 ±0.50		-	1.8	2.8	-	3.1	
		3.30 ±0.30	C _L = 50 pF,	=	2.9	4.5	-	5.0	
		5.00 ±0.50	$R_L = 500 \Omega$	=	2.4	3.6	-	4.0	
C _{IN}	Input Capacitance	0		-	2.5	-	-	_	pF
C _{PD}	C _{PD} Power Dissipation Capacitance	3.30		-	9	-	-	_	pF
	(Note 4) (Figure 7)	5.00		-	11	-	-	_	

^{4.} C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output lading and operating at 50% duty cycle. C_{PD} is related to I_{CCD} dynamic operating current by the expression:
I_{CCD} = (C_{PD}) (V_{CC}) (f_{IN}) + (I_{CC}static).

DYNAMIC SWITCHING CHARACTERISTICS

				T _A = 25°C	
Symbol	Parameter	Conditions	V _{CC}	Тур	Unit
V _{OLP}	Quiet Output Dynamic Peak V _{OL}	$C_L = 50 \text{ pF}, V_{IH} = 5.0 \text{ V}, V_{IL} = 0 \text{ V}$	5.0	0.8	V
V _{OLV}	Quiet Output Dynamic Valley V _{OL}		5.0	-0.8	V



5. C_L includes load and stray capacitance; inputs PRR = 1.0 MHz, t_W = 500 ns.

Figure 5. AC Test Circuit

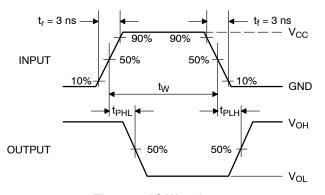
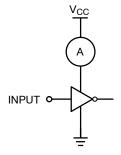


Figure 6. AC Waveforms



NOTE:

6. Input = AC Waveform; $t_r = t_f = 1.8 \text{ ns}$; PRR = 10 MHz; Duty Cycle = 50%.

Figure 7. I_{CCD} Test Circuit

NC7NZ04

DEVICE ORDERING INFORMATION

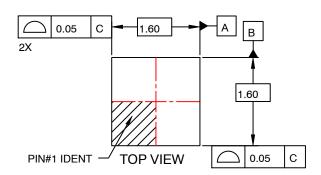
Device	Top Mark	Packages	Shipping [†]
NC7NZ04K8X	NZ04	8-Lead US8, JEDEC MO-187, Variation CA 3.1 mm Wide	3000 / Tape & Reel
NC7NZ04K8X-L22236	NZ04	8-Lead US8, JEDEC MO-187, Variation CA 3.1 mm Wide	3000 / Tape & Reel
NC7NZ04L8X	Т3	8-Lead MicroPak, 1.6 mm Wide	5000 / Tape & Reel
NC7NZ04L8X-L22185	Т3	8-Lead MicroPak, 1.6 mm Wide	5000 / Tape & Reel

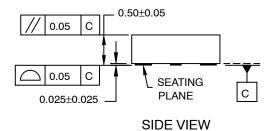
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

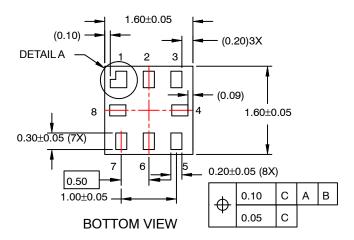
MicroPak is trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.

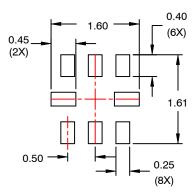
UQFN8 1.6X1.6, 0.5P CASE 523AY ISSUE O

DATE 31 AUG 2016





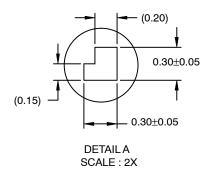




RECOMMENDED LAND PATTERN

NOTES:

- A. PACKAGE CONFORMS TO JEDEC MO-255 VARIATION UAAD.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.

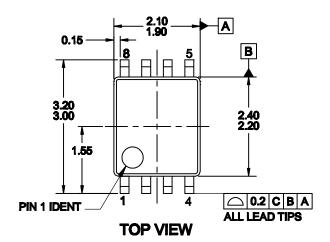


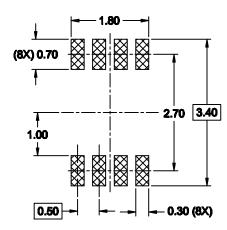
DOCUMENT NUMBER:	98AON13591G	Electronic versions are uncontrolled except when accessed directly from the Document Reposito Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	UQFN8 1.6X1.6, 0.5P		PAGE 1 OF 1		

ON Semiconductor and a retrademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

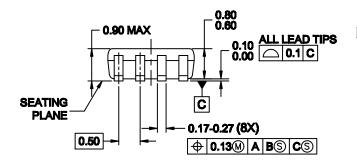
US8 CASE 846AN ISSUE O

DATE 31 DEC 2016





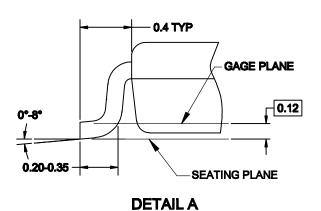
RECOMMENDED LAND PATTERN

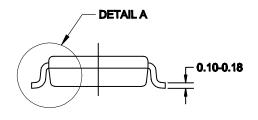


NOTES:

- A. CONFORMS TO JEDEC REGISTRATION MO-187
- **B. DIMENSIONS ARE IN MILLIMETERS.**
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1994.

SIDE VIEW





DOCUMENT NUMBER:	98AON13778G	Electronic versions are uncontrolled except when accessed directly from the Document Repos Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	US8		PAGE 1 OF 1		

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer pu

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative