

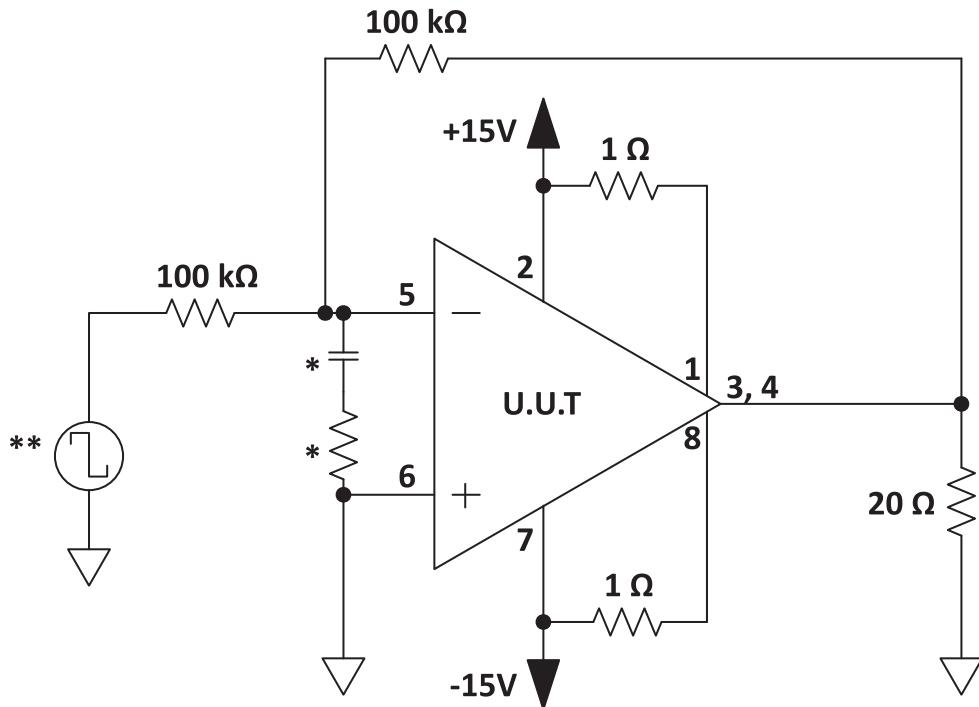
Table 4 Group A Inspection

SG Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
1 Quiescent Current	I <sub>Q</sub>	25°C	±15V	V <sub>IN</sub> =0, A <sub>V</sub> =100, R <sub>CL</sub> =0.2 Ω		40	mA
1 Input Offset Voltage	V <sub>OS</sub>	25°C	±15V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		10	mV
1 Input Offset Voltage	V <sub>OS</sub>	25°C	±7V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		11.6	mV
1 Input Offset Voltage	V <sub>OS</sub>	25°C	±19V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		10.8	mV
1 Input Bias Current, +IN	+I <sub>B</sub>	25°C	±15V	V <sub>IN</sub> = 0		200	pA
1 Input Bias Current, -IN	-I <sub>B</sub>	25°C	±15V	V <sub>IN</sub> = 0		200	pA
1 Input Offset Current	I <sub>OS</sub>	25°C	±15V	V <sub>IN</sub> = 0		100	pA
3 Quiescent Current	I <sub>Q</sub>	-55°C	±15V	V <sub>IN</sub> =0, A <sub>V</sub> =100, R <sub>CL</sub> =0.2 Ω		60	mA
3 Input Offset Voltage	V <sub>OS</sub>	-55°C	±15V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		14	mV
3 Input Offset Voltage	V <sub>OS</sub>	-55°C	±7V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		15.6	mV
3 Input Offset Voltage	V <sub>OS</sub>	-55°C	±19V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		14.8	mV
3 Input Bias Current, +IN	+I <sub>B</sub>	-55°C	±15V	V <sub>IN</sub> = 0		200	pA
3 Input Bias Current, -IN	-I <sub>B</sub>	-55°C	±15V	V <sub>IN</sub> = 0		200	pA
3 Input Offset Current	I <sub>OS</sub>	-55°C	±15V	V <sub>IN</sub> = 0		100	pA
2 Quiescent Current	I <sub>Q</sub>	125°C	±15V	V <sub>IN</sub> =0, A <sub>V</sub> =100, R <sub>CL</sub> =0.2 Ω		60	mA
2 Input Offset Voltage	V <sub>OS</sub>	125°C	±15V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		15	mV
2 Input Offset Voltage	V <sub>OS</sub>	125°C	±7V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		16.6	mV
2 Input Offset Voltage	V <sub>OS</sub>	125°C	±19V	V <sub>IN</sub> = 0, A <sub>V</sub> = 100		15.8	mV
2 Input Bias Current, +IN	+I <sub>B</sub>	125°C	±15V	V <sub>IN</sub> = 0		30	nA
2 Input Bias Current, -IN	-I <sub>B</sub>	125°C	±15V	V <sub>IN</sub> = 0		30	nA
2 Input Offset Current	I <sub>OS</sub>	125°C	±15V	V <sub>IN</sub> = 0		10	nA
4 Output Voltage, I <sub>O</sub> = 5A	V <sub>O</sub>	25°C	±9V	R <sub>L</sub> = 1 Ω, R <sub>CL</sub> = 0 Ω	5		V
4 Output Voltage, I <sub>O</sub> = 36mA	V <sub>O</sub>	25°C	±19V	R <sub>L</sub> = 500 Ω	18		V
4 Output Voltage, I <sub>O</sub> = 2A	V <sub>O</sub>	25°C	±12V	R <sub>L</sub> = 5 Ω, R <sub>CL</sub> = 0 Ω	10		V
4 Current Limits	I <sub>CL</sub>	25°C	±9V	R <sub>L</sub> = 5 Ω, R <sub>CL</sub> = 1 Ω	0.54	0.86	A
4 Stability/Noise	E <sub>N</sub>	25°C	±15V	R <sub>L</sub> =500 Ω, A <sub>V</sub> =1, C <sub>L</sub> =1.5nF		1	mV
4 Slew Rate	SR	25°C	±18V	R <sub>L</sub> = 500 Ω	13	100	V/μs
4 Open Loop Gain	A <sub>OL</sub>	25°C	±15V	R <sub>L</sub> = 500 Ω, F = 10 Hz	86		dB
4 Common Mode Rejection	CMR	25°C	±8.25V	R <sub>L</sub> = 500 Ω, F = DC, V <sub>CM</sub> = ±2.25V	70		dB

SG Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
6 Output Voltage, $I_O = 5A$	$V_O$	-55°C	±9V	$R_L = 1 \Omega$ , $R_{CL} = 0 \Omega$	5		V
6 Output Voltage, $I_O = 36mA$	$V_O$	-55°C	±19V	$R_L = 500 \Omega$	18		V
6 Output Voltage, $I_O = 2A$	$V_O$	-55°C	±12V	$R_L = 5 \Omega$ , $R_{CL} = 0 \Omega$	10		V
6 Stability/Noise	$E_N$	-55°C	±15V	$R_L = 500 \Omega$ , $A_V = 1$ , $C_L = 1.5nF$		1	mV
6 Slew Rate	SR	-55°C	±18V	$R_L = 500 \Omega$	13	100	V/µs
6 Open Loop Gain	$A_{OL}$	-55°C	±15V	$R_L = 500 \Omega$ , $F = 10$ Hz	86		dB
6 Common Mode Rejection	CMR	-55°C	±8.25V	$R_L = 500 \Omega$ , $F = DC$ , $V_{CM} = ±2.25V$	70		dB
5 Output Voltage, $I_O = 3A$	$V_O$	125°C	±7V	$R_L = 1 \Omega$ , $R_{CL} = 0 \Omega$	3		V
5 Output Voltage, $I_O = 36mA$	$V_O$	125°C	±19V	$R_L = 500 \Omega$	18		V
5 Output Voltage, $I_O = 2A$	$V_O$	125°C	±12V	$R_L = 5 \Omega$ , $R_{CL} = 0 \Omega$	10		V
5 Stability/Noise	$E_N$	125°C	±15V	$R_L = 500 \Omega$ , $A_V = 1$ , $C_L = 1.5nF$		1	mV
5 Slew Rate	SR	125°C	±18V	$R_L = 500 \Omega$	8.5	100	V/µs
5 Open Loop Gain	$A_{OL}$	125°C	±15V	$R_L = 500 \Omega$ , $F = 10$ Hz	86		dB
5 Common Mode Rejection	CMR	125°C	±8.25V	$R_L = 500 \Omega$ , $F = DC$ , $V_{CM} = ±2.25V$	70		dB

## BURN IN CIRCUIT

Figure 1: Burn In Circuit



\*These components are used to stabilize device due to poor high frequency characteristics of burn in board.

\*\*Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.

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