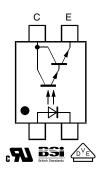




## Vishay Semiconductors

# **Optocoupler, Photodarlington Output**





#### **FEATURES**

- High isolation test voltage 5300 V<sub>RMS</sub>
- Standard plastic DIP-4 package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





### **AGENCY APPROVALS**

- UL file no. E52744 system code H, double protection
- DIN EN 60747-5-2 (VDE 0884), IEC 60747-5-5
- DIN EN 60747-5-5 (VDE 0884) pending
- BSI IEC 60950; IEC 60065

#### **DESCRIPTION**

The SFH655A is optically coupled isolators with a gallium arsenide infrared LED and a silicon photodarlington detector. Switching can be achieved while maintaining a high degree of isolation between driving and load circuits.

This optocouplers can be used to replace reed and mercury relays with advantages of long life, high speed switching and elimination of magnetic fields.

ORDERING INFORMATION				
S F H 6 #	# A - # # # DIP Option 9			
PART NUMBER	PACKAGE OPTION 7.62 mm > 0.1 mm			
AGENCY CERTIFIED/PACKAGE	CTR (%)			
cUL, VDE	> 600			
DIP-4	SFH655A			
SMD-4, option 9	SFH655A-X009			

#### Note

· For additional information on the available options refer to option information

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Peak reverse voltage		$V_{RM}$	6	V		
Forward continuous current		I <sub>F</sub>	60	mA		
Surge forward current	t <sub>p</sub> ≤ 10µs	I <sub>FSM</sub>	2.5	Α		
Derate linearly from 25 °C			1.33	mW/°C		
Power dissipation		P <sub>diss</sub>	100	mW		
OUTPUT						
Collector emitter breakdown voltage		BV <sub>CEO</sub>	55	V		
Emitter collector breakdown voltage		BV <sub>ECO</sub>	6	V		
Collector (load) current		I <sub>C</sub>	125	mA		
Derate linearly from 25 °C			2	mW/°C		
Power dissipation		P <sub>diss</sub>	150	mW		





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ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
COUPLER						
Derate linearly from 25 °C			3.33	mW/°C		
Total power dissipation		P <sub>tot</sub>	250	mW		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature range		T <sub>amb</sub>	-55 to +100	°C		
Soldering temperature (1)	max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm	$T_{sld}$	260	°C		

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP)

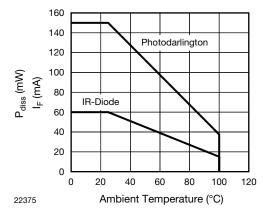


Fig. 1 - Power Dissipation vs. Ambient Temperature

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 10 \text{ mA}$		$V_{F}$	-	1.15	1.5	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	-	0.02	10	μA
Capacitance	$V_R = 0 V, f = 1 MHz$		Co	-	50	-	pF
OUTPUT							
Collector emitter breakdown voltage	I <sub>CE</sub> = 100 μA		BV <sub>CEO</sub>	55	-	-	V
Emitter collector breakdown voltage	$I_{EC} = 10 \mu A$		BV <sub>ECO</sub>	6	-	-	V
Collector emitter dark current	$V_{CE} = 40 \text{ V}$		I <sub>CEO</sub>	-	12	400	nA
Collector emitter capacitance	$V_{CE} = 0 V, f = 1 MHz$		C <sub>CE</sub>	-	13.5	-	pF
COUPLER							
Collector emitter saturation voltage	$I_F = 20 \text{ mA}, I_C = 5 \text{ mA}$	SFH655A	V <sub>CEsat</sub>	-	-	1	V
Coupling capacitance	$V_{I-O} = 0 V$ , $f = 1 MHz$		C <sub>C</sub>	-	0.45	-	pF

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements





# Vishay Semiconductors

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UNIT							
Current transfer ratio	$I_F = 1 \text{ mA}, V_{CE} = 2 \text{ V}$	SFH655A	CTR	600	-	-	%

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	$V_{RMS}$
Maximum transient isolation voltage		V <sub>IOTM</sub>	10 000	V
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	V
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
isolation registration	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω
Output safety power		P <sub>SO</sub>	400	mW
Input safety current		I <sub>SI</sub>	275	mA
Input safety temperature		T <sub>SI</sub>	175	°C
Creepage distance	Standard DIP-4		≥ 7	mm
Clearance distance	Standard DIP-4		≥ 7	mm
Insulation thickness		DTI	≥ 0.4	mm
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	1.669	kV
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s}, (see fig. 2)$	V <sub>pd</sub>	1.424	kV

#### Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

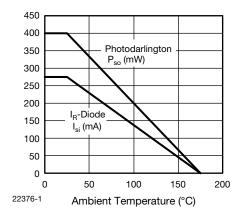


Fig. 2 - Derating Diagram

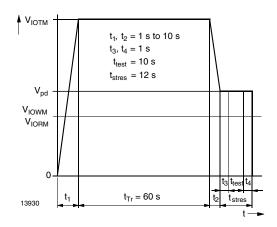


Fig. 3 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2 (VDE 0884); IEC 60747-5-5



## Vishay Semiconductors

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time (fig. 10, test circuit 1)	$V_{CC}$ = 10 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	SFH612A	t <sub>on</sub>		16		μs
Turn-off time (fig. 10, test circuit 1)	$V_{CC}$ = 10 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	SFH612A	t <sub>off</sub>		15		μs
Rise time (fig. 10, test circuit 1)	$V_{CC}$ = 10 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	SFH612A	t <sub>r</sub>		14		μs
Fall time (fig. 10, test circuit 1)	$V_{CC}$ = 10 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	SFH612A	t <sub>f</sub>		14		μs
Turn-on time (fig. 11, test circuit 2)	$V_{CC}$ = 2 V, $I_C$ = 10 mA, $R_L$ = 100 $\Omega$	SFH655A	t <sub>on</sub>		31		μs
Turn-off time (fig. 11, test circuit 2)	$V_{CC}$ = 2 V, $I_C$ = 10 mA, $R_L$ = 100 $\Omega$	SFH655A	t <sub>off</sub>		55		μs
Rise time (fig. 11, test circuit 2)	$V_{CC}$ = 2 V, $I_C$ = 10 mA, $R_L$ = 100 $\Omega$	SFH655A	t <sub>r</sub>		27	250	μs
Fall time (fig. 11, test circuit 2)	$V_{CC}$ = 2 V, $I_C$ = 10 mA, $R_L$ = 100 $\Omega$	SFH655A	t <sub>f</sub>		56	200	μs

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

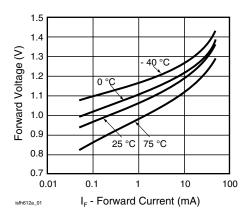


Fig. 4 - Forward Voltage vs. Forward Current

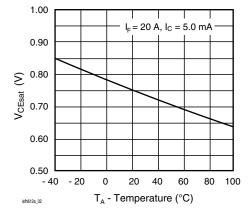


Fig. 5 - Collector Emitter Saturation Voltage vs. Temperature

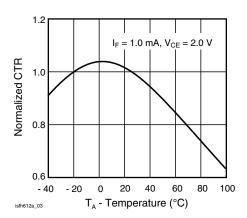


Fig. 6 - Normalized CTR vs. Temperature

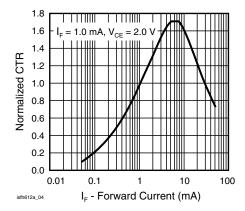


Fig. 7 - Normalized CTR vs. Forward Current



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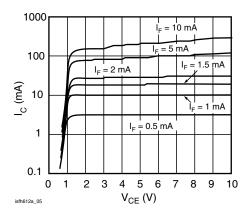


Fig. 8 - Collector Current vs. Collector Emitter Voltage

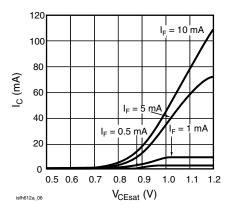


Fig. 9 - Collector Current vs. Collector Emitter Saturation Voltage

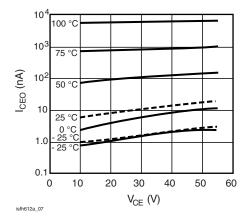


Fig. 10 - Collector Emitter Dark Current vs. Collector Emitter Voltage over Temperature

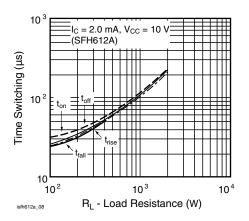


Fig. 11 - Switching Time vs. Load Resistor

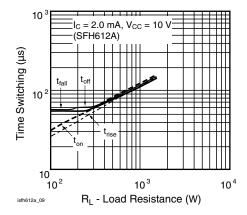


Fig. 12 - Switching Time vs. Load Resistor

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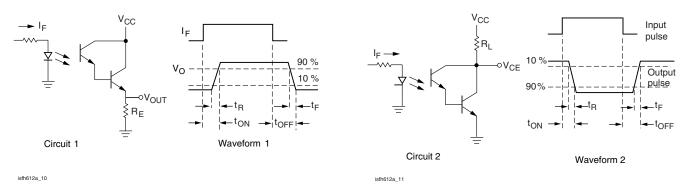
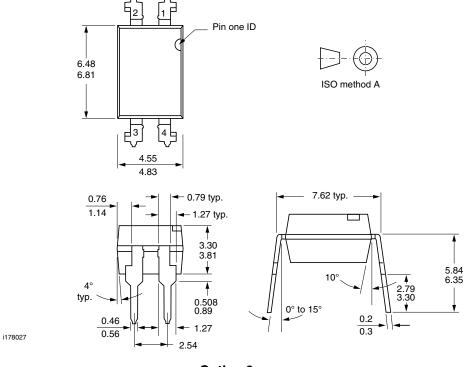


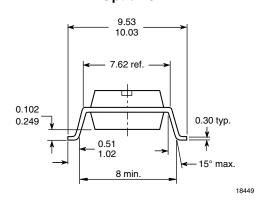
Fig. 13 - Switching Time Test Circuit and Waveforms

Fig. 14 - Switching Time Test Circuit and Waveforms

#### **PACKAGE DIMENSIONS** in millimeters



### Option 9

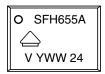




### SFH655A

# Vishay Semiconductors

### **PACKAGE MARKING**



#### Note

• VDE logo is only printed on option 1 parts. Option information is not marked on the part



# **Footprint and Schematic Information**

Vishay Semiconductors

# Footprint and Schematic Information for SFH655A

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
SFH655A	www.snapeda.com/parts/SFH655A/Vishay/view-part
SFH655A-X009	www.snapeda.com/parts/SFH655A-X009/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishav.com.





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