



# P-Channel 20-V (D-S) MOSFET

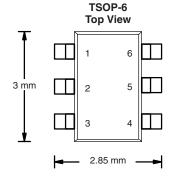
PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 20	$0.115$ at $V_{GS} = -4.5 \text{ V}$	- 2.8	3.2 nC			
	0.205 at V <sub>GS</sub> = - 2.5 V	- 2.1	3.2 110			

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- PWM Optimized
- 100 % R<sub>q</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

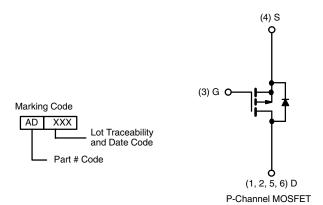






Ordering Information: Si3451DV-T1-E3 (Lead (Pb)-free)

Si3451DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20	V	
Gate-Source Voltage		$V_{GS}$	± 12		
	T <sub>C</sub> = 25 °C		- 2.8		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 2.3		
Continuous Diain Current (1) = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 2.6 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C	1	- 2.1 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	- 10		
	T <sub>C</sub> = 25 °C		- 1.76		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 1.04 <sup>b, c</sup>		
Maximum Power Dissipation	T <sub>C</sub> = 25 °C		2.1		
	T <sub>C</sub> = 70 °C	1 ,	1.3	10/	
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.25 <sup>b, c</sup>	w	
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>		
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	$R_{thJA}$	75	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	70	85		

#### Notes:

- a. Based on  $T_C$  = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- $c. \quad t=5 \ s.$
- d. Maximum under Steady State conditions is 120 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L = 250 uA		- 16.7		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- I <sub>D</sub> = - 250 μA		2.1			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 0.6		- 1.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1		
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 10			Α	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = -4.5 \text{ V}, I_D = -2.6 \text{ A}$		0.092	0.115	Ω	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 1.9 A		0.164	0.205		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 2.6 A		5.5		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			250		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		80			
Reverse Transfer Capacitance	C <sub>rss</sub>			55			
Total Cata Charge	$Q_{g}$	$V_{DS} = -10 \text{ V}, V_{GS} = -5.0 \text{ V}, I_{D} = -2.6 \text{ A}$		3.4	5.1	nC	
Total Gate Charge	<b>Q</b> g			3.2	5		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -2.6 \text{ A}$		0.5			
Gate-Drain Charge	$Q_{gd}$			1.4			
Gate Resistance	$R_{g}$	f = 1 MHz		8.5	13	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			9	14		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_{L} = 4.76 \Omega$		30	45	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 2.1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		32	48		
Fall Time	t <sub>f</sub>			16	24		
<b>Drain-Source Body Diode Characteristic</b>	s						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 1.7	А	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 10		
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = - 2.1 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			17	26	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			5	6	nC	
Reverse Recovery Fall Time	t <sub>a</sub>			14		nc	
Reverse Recovery Rise Time	t <sub>b</sub>			3		ns	

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

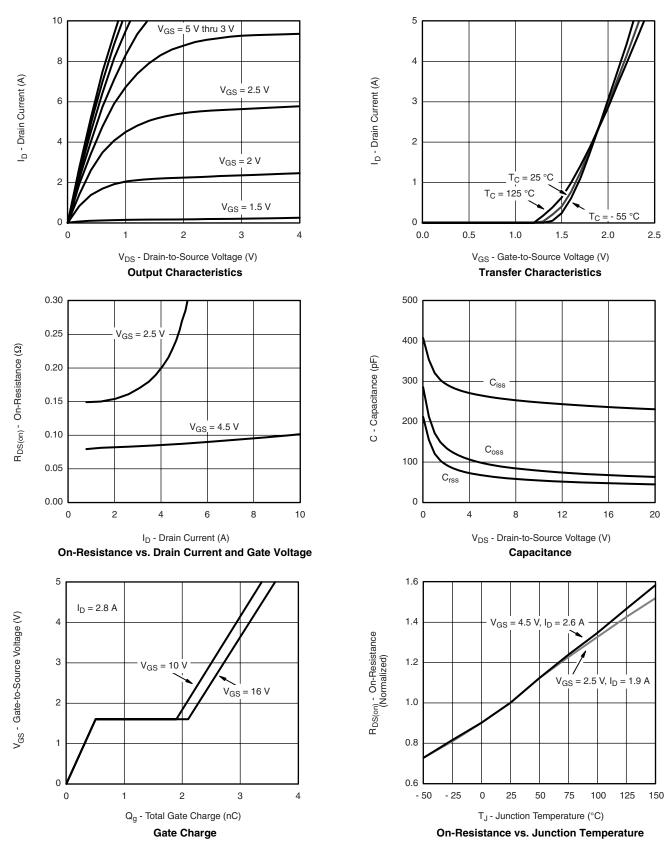
b. Guaranteed by design, not subject to production testing.





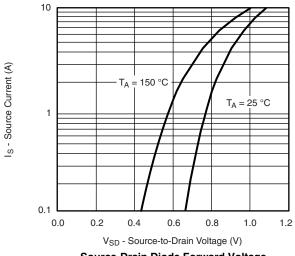


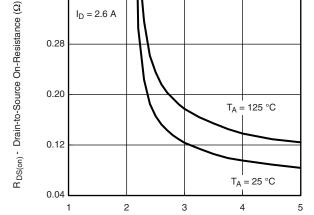
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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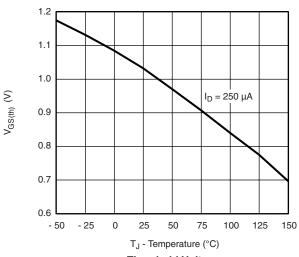
0.36

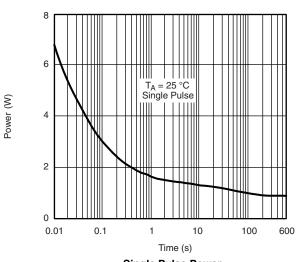
0.28

I<sub>D</sub> = 2.6 A

Source-Drain Diode Forward Voltage

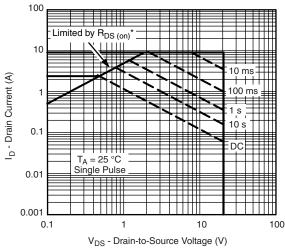








Single Pulse Power



\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

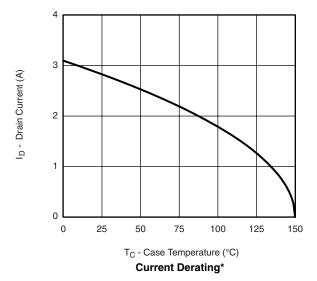
**Safe Operating Area** 

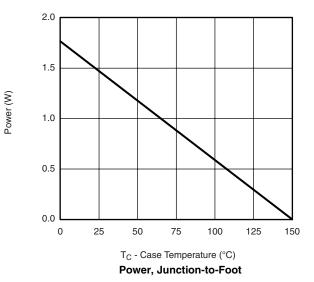




Si3451DV Vishay Siliconix

# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



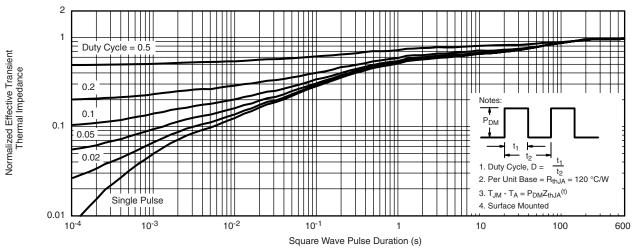


 $<sup>^{\</sup>star}$  The power dissipation P<sub>D</sub> is based on T<sub>J(max)</sub> = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package

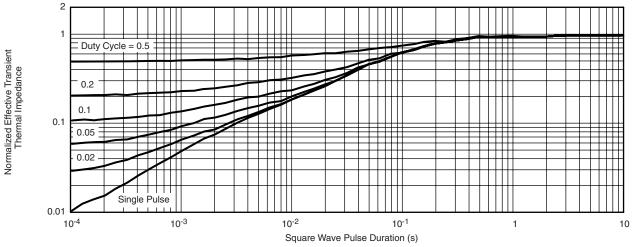
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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