Vishay Siliconix

P-Channel 12 V (D-S) MOSFET



Marking code: BR

PRODUCT SUMMARY	
V _{DS} (V)	-12
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5 \text{ V}$	0.0178
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -2.5 \text{ V}$	0.0247
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -1.8 \text{ V}$	0.0388
Q _g typ. (nC)	22.8
I _D (A) ^{a, d}	-8
Configuration	Single

FEATURES

- TrenchFET® Gen III p-channel power MOSFET
- $R_{DS(on)}$ rating at $V_{GS} = -1.8 \text{ V}$
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

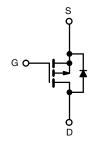


RoHS COMPLIANT

HALOGEN FREE

APPLICATIONS

- · Load switch
- PA switch



P-Channel MOSFET

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	Si3473DDV-T1-GE3

ABSOLUTE MAXIMUM RATING	S (T _A = 25 °C, u	ınless otherv	wise noted)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	-12	V
Gate-source voltage		V_{GS}	± 8	v
	T _C = 25 °C		-8 ^a	
Continuous dusin surrent /T 150 °C\	T _C = 70 °C] ,	-8 a	
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	I _D	-8.7 ^{b, c}	
	T _A = 70 °C		-6.9 ^{b, c}	А
Pulsed drain current (t = 100 µs)		I _{DM}	-30	
Canting and a summer	T _C = 25 °C		-3	
Continuous source-drain diode current	T _A = 25 °C	- I _S	-1.67 ^{b, c}	
	T _C = 25 °C		3.6	
Maximum power dissipation	T _C = 70 °C		2.3	W
	T _A = 25 °C	P _D	2 b, c	VV
	T _A = 70 °C		1.3 ^{b, c}	
Operating junction and storage temperature range		T _J , T _{stq}	-55 to +150	°C

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient ^b	t ≤ 5 s	R _{thJA}	50	62.5	°C/W	
Maximum junction-to-case (drain)	Steady state	R _{thJC}	28	35	C/VV	

Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 110 °C/W

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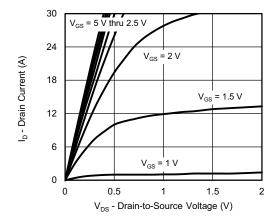
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	L					
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-12	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	J 050 A	-	-5.5	-	1400
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	2.5	-	mV/°C
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-0.4	-	-1	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 100	nA
Zoro goto voltogo droin overent		V _{DS} = -12 V, V _{GS} = 0 V	-	-	-1	
Zero gate voltage drain current	I _{DSS}	V _{DS} = -12 V, V _{GS} = 0 V, T _J = 70 °C	-	-	-10	μA
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -4.5 \text{ V}$	-10	-	-	Α
		V _{GS} = -4.5 V, I _D = -8.7 A	-	0.0145	0.0178	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -2.5 V, I _D = -7.3 A	-	0.0198	0.0247	Ω
		V _{GS} = -1.8 V, I _D = -2.6 A	-	0.0277	0.0388	
Forward transconductance ^a	9 _{fs}	$V_{DS} = -6 \text{ V}, I_{D} = -8.7 \text{ A}$	-	30	-	S
Dynamic ^b						
Input capacitance	C _{iss}		-	1975	-	pF
Output capacitance	C _{oss}	V _{DS} = -6 V, V _{GS} = 0 V, f = 1 MHz	-	520	-	
Reverse transfer capacitance	C _{rss}		-	501	-	
	Qg	$V_{DS} = -6 \text{ V}, V_{GS} = -8 \text{ V}, I_{D} = -8.7 \text{ A}$	-	38	57	nC
Total gate charge			-	22.8	35	
Gate-source charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -8.7 \text{ A}$	-	1.9	-	
Gate-drain charge	Q_{gd}		-	5.7	-	
Gate resistance	Rg	f = 1 MHz	0.82	4.1	8.2	Ω
Turn-on delay time	t _{d(on)}		-	22	35	
Rise time	t _r	$V_{DD} = -6 \text{ V}, R_L = 0.9 \Omega, I_D \cong -6.9 \text{ A},$	-	28	45	1
Turn-off delay time	t _{d(off)}	V_{GEN} = -4.5 V, R_g = 1 Ω	-	50	75	
Fall time	t _f		-	35	55	
Turn-on delay time	t _{d(on)}		-	12	20	ns
Rise time	t _r	$V_{DD} = -6 \text{ V}, R_L = 0.9 \Omega, I_D \cong -6.9 \text{ A},$	-	20	30	-
Turn-off delay time	t _{d(off)}	$V_{GEN} = -8 \text{ V}, R_g = 1 \Omega$	-	45	70	
Fall time	t _f		-	30	45	
Drain-Source Body Diode Characteristi	cs					
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-8	_
Pulse diode forward current	I _{SM}		-	-	-30	A
Body diode voltage	V_{SD}	I _S = -6.9 A, V _{GS} = 0 V	-	-0.8	-1.2	V
Body diode reverse recovery time	t _{rr}		-	38	57	ns
Body diode reverse recovery charge	Q _{rr}		-	18	27	nC
Reverse recovery fall time	ta	$I_F = -6.9 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 °\text{C}$	-	12	-	
Reverse recovery rise time	t _b		-	26	-	ns

Notes

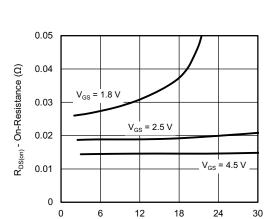
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



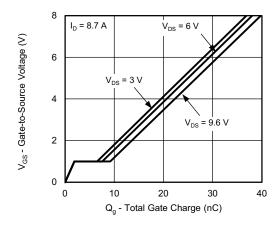


Output Characteristics

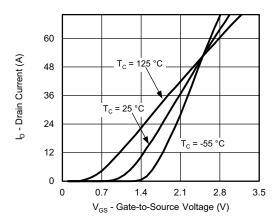


On-Resistance vs. Drain Current and Gate Voltage

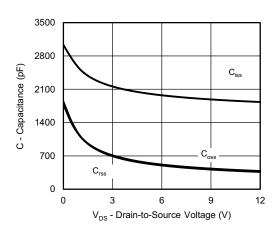
I_D - Drain Current (A)



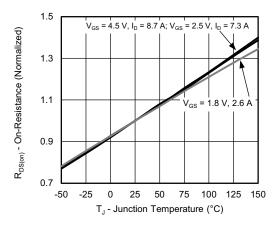
Gate Charge



Transfer Characteristics

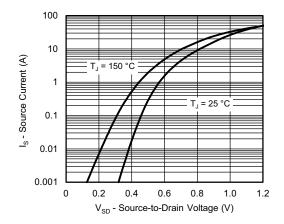


Capacitance

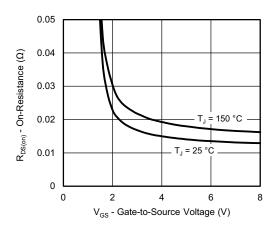


On-Resistance vs. Junction Temperature

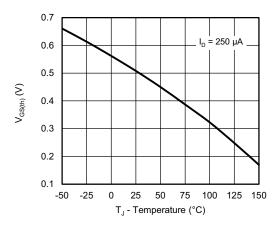




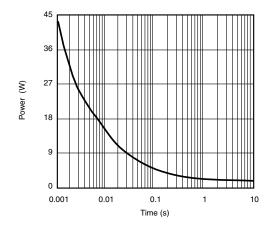
Source-Drain Diode Forward Voltage



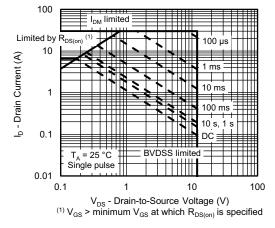
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

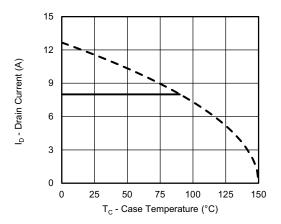


Single Pulse Power, Junction-to-Ambient

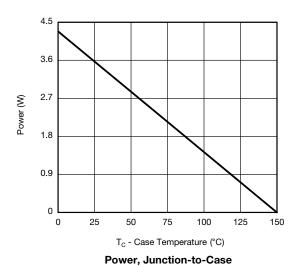


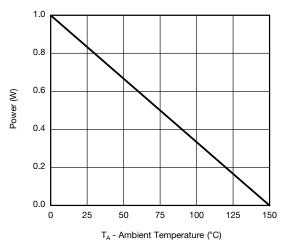
Safe Operating Area, Junction-to-Ambient





Current Derating a



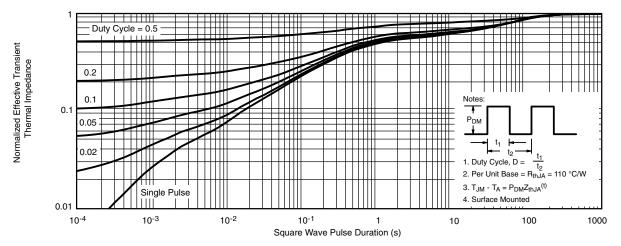


Power, Junction-to-Ambient

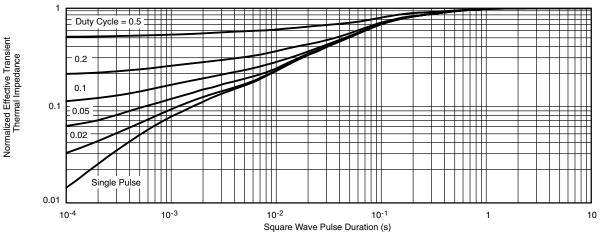
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

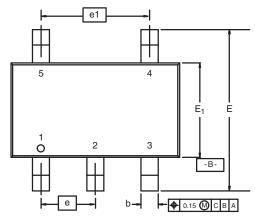
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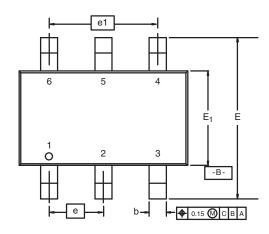




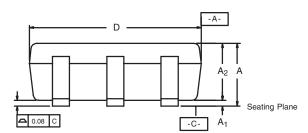
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C

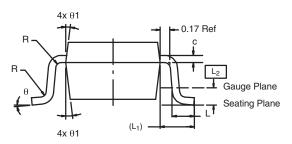




5-LEAD TSOP





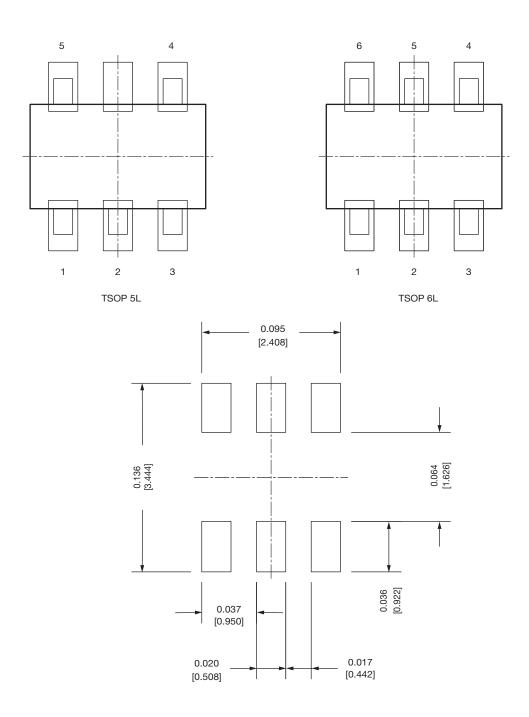


	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref			0.024 Ref		
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200 18-Dec-06



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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