

RoHS

COMPLIANT HALOGEN FREE Available

Vishay Siliconix

Dual N-Channel 30-V (D-S) MOSFET with Schottky Diode

FEATURES

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Definition

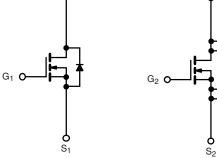
LITTLE FOOT[®] Plus

D₁

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
30	0.022 at V _{GS} = 10 V	7.5		
	0.030 at V _{GS} = 4.5 V	6.5		

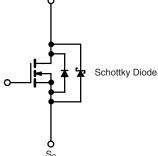
SCHOTTKY PRODUCT SUMMARY					
V _{DS} (V)	V _{SD} (V) Diode Forward Voltage	I _F (A)			
30	0.50 V at 1.0 A	2.0			

SO-8 S_1 D₁ 8 1 G1 D_1 7 S_2 D_2 6 3 G_2 D_2 5 Top View



• Halogen-free According to IEC 61249-2-21

Compliant to RoHS directive 2002/95/EC



Ordering Information: Si4808DY-T1-E3 (Lead (Pb)-free) Si4808DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

N-Channel MOSFET

 D_2

ABSOLUTE MAXIMUM RATINGS	Γ _A = 25 °C, unl	ess otherwise	e noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
	T _A = 25 °C		7.5	5.7	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C	D	6.0	4.6	
Pulsed Drain Current		I _{DM}	30		A
Continuous Source Current (Diode Conduction) ^a		I _S	1.7	1.7 0.9	
	T _A = 25 °C	P _D	2.0	1.1	w
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.7	vv
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS								
			MOSFET		Schottky			
Parameter		Symbol	Тур.	Max.	Тур.	Max.	Unit	
Mauinum lunation to Ambianta	t ≤ 10 s	R _{thJA}	52	62.5	53	62.5	°C/W	
Maximum Junction-to-Ambient ^a	Steady-State		93	110	93	110		
Maximum Junction-to-Foot (Drain)	Steady-State	R _{thJC}	35	40	35	40		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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Parameter	Symbol	I Test Conditions			Typ. ^a	Max.	Unit
Static					•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$		0.8			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$				± 100	nA
		$V_{DS} = 24 V, V_{GS} = 0 V$	Ch-1			1	
Zero Gate Voltage Drain Current	Inne		Ch-2			100	
Zero Gale Voltage Dialit Current	IDSS	V _{DS} = 24 V, V _{GS} = 0 V, T _J = 85 °C	Ch-1			15	μΑ
		VDS = 24 V, VGS = 0 V, 1J = 00 O	Ch-2			2000	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$		20			Α
	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$			0.018	0.022	Ω
Drain-Source On-State Resistance ^b	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_{D} = 6.5 \text{ A}$		0.024	0.030		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 7.5 A			22		S
h	V _{SD}	I _S = 1 A, V _{GS} = 0 V	Ch-1		0.8	1.2	V
Diode Forward Voltage ^b			Ch-2		0.47	0.5	1 [×]
Dynamic ^a							
Total Gate Charge	Qg				13	20	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 7$.5 A		2		nC
Gate-Drain Charge	Q _{gd}				2.7		
Gate Resistance	Rg			0.5		3.2	Ω
Turn-On Delay Time	t _{d(on)}				8	16	
Rise Time	t _r	V _{DD} = 15 V, R _I = 15 Ω			10	20	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 6 \Omega$			21	40	20
Fall Time	t _f				10	20	ns
Source-Drain Reverse Recovery	Source-Drain Reverse Recovery t_{rr} $I_r = 1.7 \text{ A}$, $dI/dt = 100 \text{ A/us}$		Ch-1		40	80	
Time			Ch-2		32	70	

Notes:

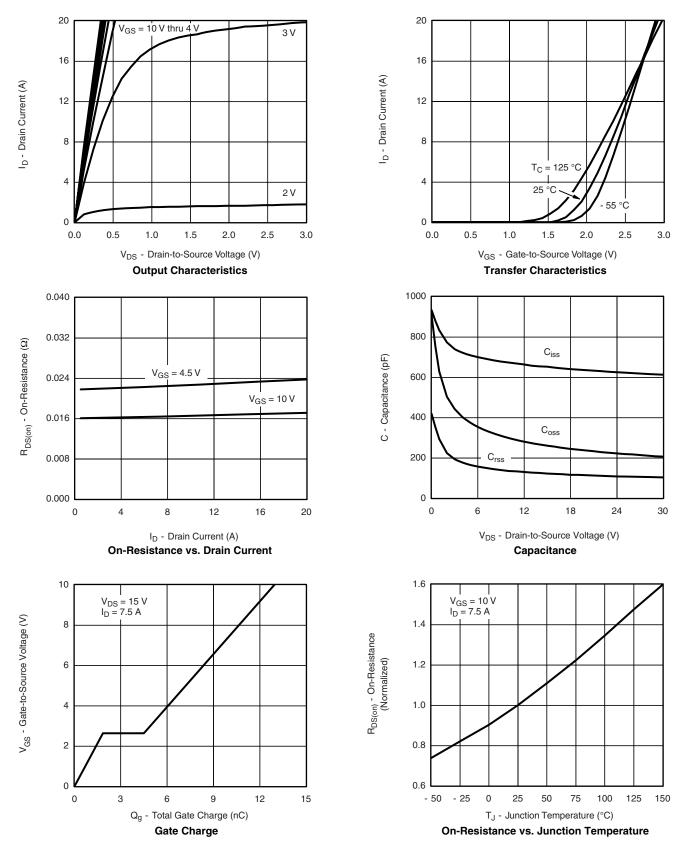
a. Guaranteed by design, not subject to production testing. b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

SCHOTTKY SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage Drop	V _F	I _F = 1.0 A		0.47	0.50	v		
		I _F = 1.0 A, T _J = 125 °C		0.36	0.42			
Maximum Reverse Leakage Current	I _{rm}	V _R = 30 V		0.004	0.100	mA		
		$V_{R} = 30 \text{ V}, \text{ T}_{J} = 100 ^{\circ}\text{C}$		0.7	10			
		V_{R} = - 30 V, T_{J} = 125 °C		3.0	20			
Junction Capacitance	CT	V _R = 10 V		50		pF		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

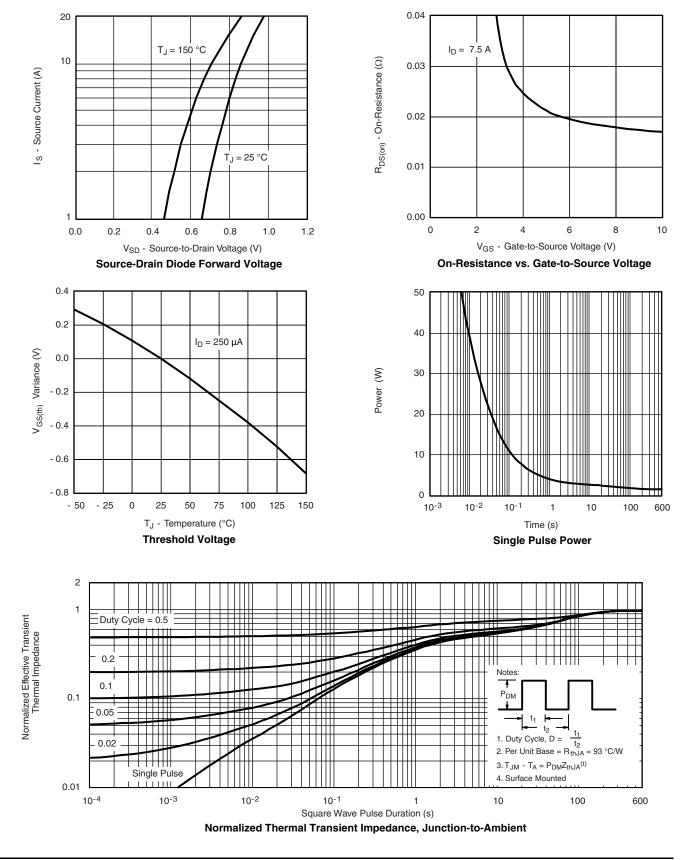


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Si4808DY

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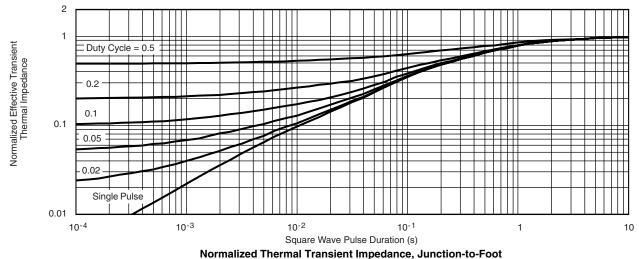






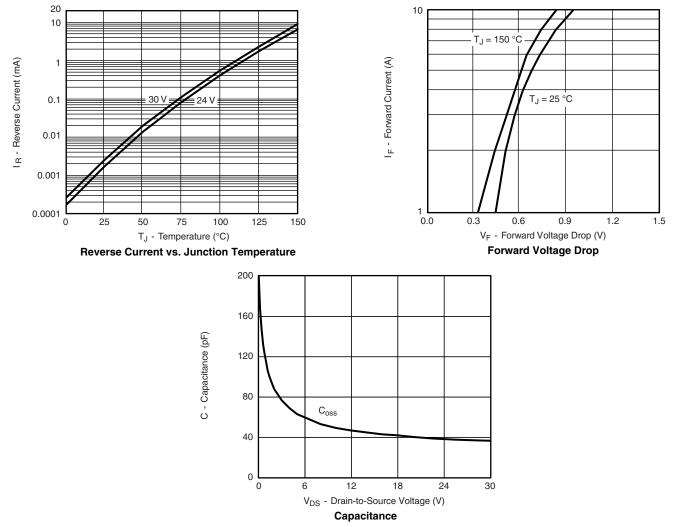
Si4808DY

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MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71157.



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