



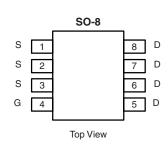
N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$ $I_{D}\left(\Omega\right)$			
20	0.005 at V _{GS} = 4.5 V	21		
	0.0075 at V _{GS} = 2.5 V	17		

FEATURES

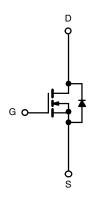
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFET
- 100 % R_g Tested





Ordering Information: Si4876DY-T1-E3 (Lead (Pb)-free)

Si4876DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise n	oted		
Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	20		V
Gate-Source Voltage		V _{GS}	± 12		
Continuous Dusin Comment /T 450 °C)	T _A = 25 °C	- I _D	21	14	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		15	10	1
Pulsed Drain Current		I _{DM}	50		Α
Avalanche Current	L = 0.1 mH	I _{AS}	42 88		
Single Avalanche Energy	L = 0.1 IIII	E _{AS}			mJ
Continuous Source Current (Diode Conduction) ^a		I _S	3	1.3	mS
M	T _A = 25 °C	P _D	3.6	1.6	W
Maximum Power Dissipation ^a	T _A = 85 °C		1.9	0.8	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manipulation to Applicate	t ≤ 10 s	- R _{thJA}	29	35		
Maximum Junction-to-Ambient ^a	Steady State		67	80	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16		

Notes

a. Surface Mounted on 1" x 1" FR4 board.

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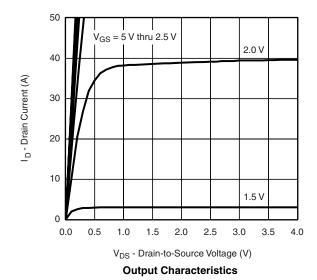
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.6			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			1		
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			20	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	50			Α	
Drain-Source On-State Resistance ^a	В	$V_{GS} = 4.5 \text{ V}, I_D = 21 \text{ A}$		0.0037	0.005	Ω	
	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 17 \text{ A}$		0.0058	0.0075		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 21 A		17		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 3 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Dynamic ^b			•	•			
Total Gate Charge	Qg			55	80	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 21 \text{ A}$		13			
Gate-Drain Charge	Q_{gd}			11		1	
Gate Resistance	R_g		2.0	2.7	4.6	Ω	
Turn-On Delay Time	t _{d(on)}			40	60		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		30	45	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		175	260		
Fall Time	t _f			70	105		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 3 A, dI/dt = 100 A/μs		56	85		

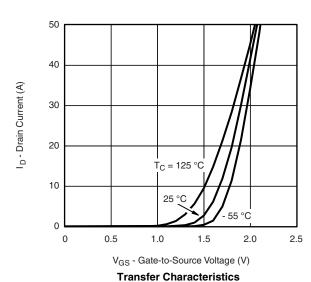
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

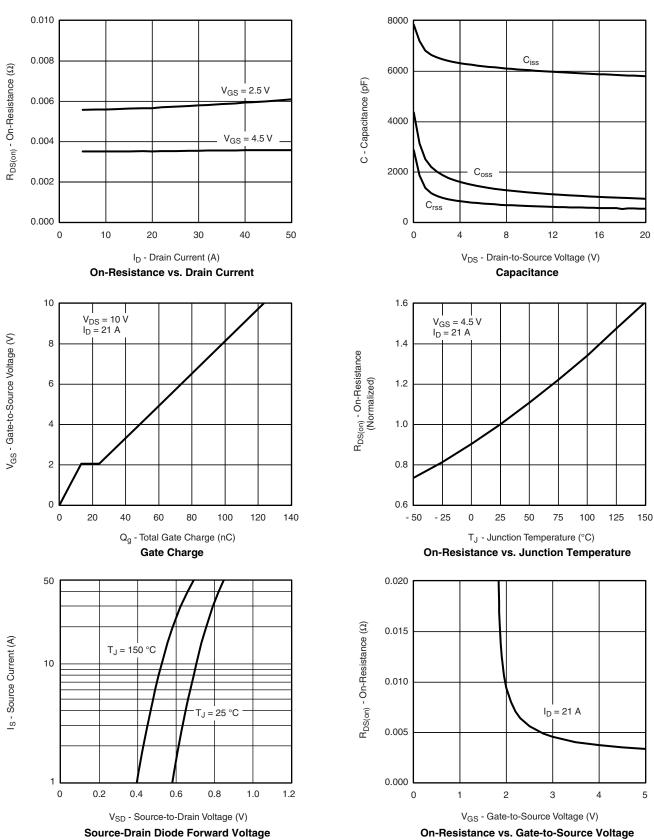








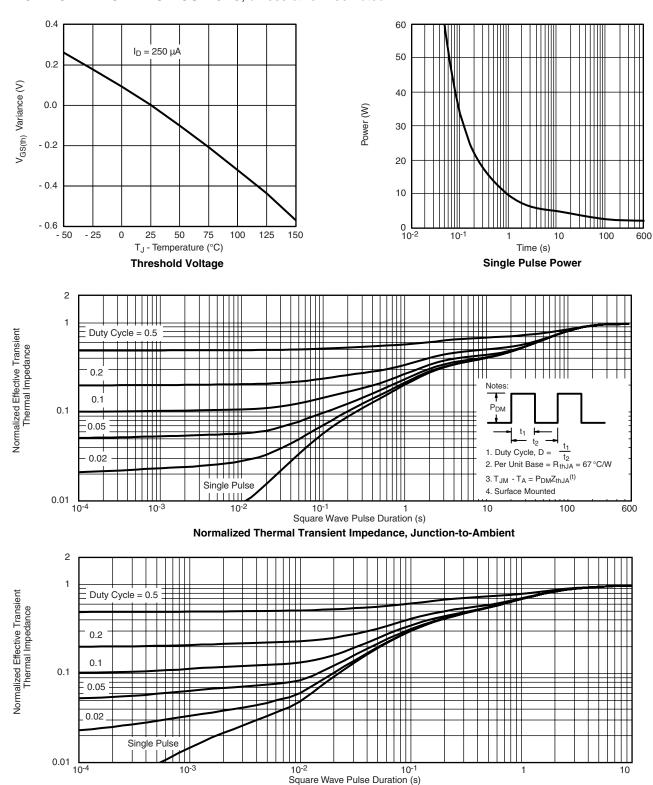
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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Normalized Thermal Transient Impedance, Junction-to-Foot



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