SN54BCT29863B, SN74BCT29863B 9-BIT BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

SCBS015D - NOVEMBER 1988 - REVISED NOVEMBER 1993

- BiCMOS Design Substantially Reduces I_{CCZ}
- Functionally Equivalent to 'ALS29863 and AMD Am29863A
- Power-Up High-Impedance State
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015
- Package Options Include Plastic Small-Outline Packages (DW), Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (JT, NT)

description

These 9-bit transceivers are designed for asynchronous communication between data buses. The control-function implementation allows for maximum flexibility in timing.

These devices allow data transmission from the A bus to the B bus or from the B bus to the A bus, depending upon the logic levels at the output-enable (OEBA and OEAB) inputs.

The outputs are in the high-impedance state during power-up and power-down conditions. The outputs remain in the high-impedance state while the device is powered down.

The SN54BCT29863B is characterized for operation over the full military temperature range of -55° C to 125° C. The SN74BCT29863B is characterized for operation from 0°C to 70°C.

	•		•
OEBA1	1	U ₂₄	Vcc
A1 [2	23] B1
A2 [3	22] B2
A3 [4	21] B3
A4 [5	20] B4
A5 [6	19] B5
A6 [7	18] B6
A7 [8	17] B7
A8 [9	16] B8
A9 [10	15] B9
OEBA2	11	14	OEAB2
GND [12	13	OEAB1

SN54BCT29863B ... JT OR W PACKAGE

SN74BCT29863B . . . DW OR NT PACKAGE

(TOP VIEW)

SN54BCT29863B . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

FUNCTION TABLE								
	INP							
OEAB1	OEAB2	OEBA1	OEBA2	OPERATION				
L	L	L	L	Latch A and B				
L	L	Н	Х					
L	L	Х	Н	A to B				
Н	Х	L	L	D to A				
Х	Н	L	L	B to A				
Н	Х	Н	Х					
н	Х	Х	Н	Isolation				
Х	Н	Х	Н	isoidlion				
Х	Н	Н	Х					

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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logic symbol[†]



logic diagram (positive logic)



To Eight Other Channels

[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the DW, JT, NT, and W packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[‡]

Supply voltage range Vcc	-0.5 V to 7 V
Input voltage range (I/O ports) (see Note 1)	0.5 V to 5.5 V
Input voltage range (excluding I/O ports) (see Note 1)	\ldots -0.5 V to 7 V
Voltage range applied to any output in the high state	$\dots -0.5$ V to V _{CC}
Input clamp current	
Current into any output in the low state: SN54BCT29863B	48 mA
SN74BCT29863B	96 mA
Operating free-air temperature range: SN54BCT29863B	. $-55^\circ C$ to $125^\circ C$
SN74BCT29863B	\ldots 0°C to 70°C
Storage temperature range	. $-65^\circ C$ to $150^\circ C$

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The negative input voltage ratings may be exceeded if the input current rating is observed.



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recommended operating conditions

		SN54BCT29863B			SN74	LINUT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
IК	Input clamp current			-18			-18	mA
IOH	High-level output current			-15			-24	mA
IOL	Low-level output current			24			48	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		TEAT CONDITIONS		SN54	SN54BCT29863B			SN74BCT29863B				
	PARAMETER TEST CONDITIONS		MIN	түр†	MAX	MIN	түр†	MAX	UNIT			
VIK		V _{CC} = 4.5 V,	lj = – 18 mA			-1.2			-1.2	V		
			I _{OH} = -15 mA	2.4	3.3		2.4	3.3				
∨он		VCC = 4.5 V	$I_{OH} = -24 \text{ mA}$				2	3.1		V		
		V _{CC} = 4.75 V,	$I_{OH} = -3 \text{ mA}$				2.7					
			I _{OL} = 24 mA		0.35	0.5						
VOL		$V_{CC} = 4.5 V$	I _{OL} = 48 mA					0.35	0.5	V		
Ιį		V _{CC} = 5.5 V,	V _I = 5.5 V			0.1			0.1	mA		
	Control inputs	V _{CC} = 5.5 V,	V 07V			20			20	μA		
ΊΗ	A or B port [‡]		$V_{ } = 2.7 V$			20			20			
	Control inputs					-0.2			-0.2			
١L	A or B port [‡]	VCC = 5.5 V,	VI = 0.5 V			-0.2			-0.2	2 mA		
IIO(off)	§	$V_{CC} = 0,$	V _O = 2.7 V			0.1			0.1	mA		
IOS		V _{CC} = 5.5 V,	VO = 0	-75		-250	-75		-250	mA		
			Outputs high		18	30		18	30			
ICC		V _{CC} = 5.5 V	Outputs low		30	45		30	45	mA		
			Outputs disabled		6.5	12		6.5	12			
Ci		$V_{CC} = 5 V,$	V _I = 2.5 V or 0.5 V		6			6		pF		
Cio		V _{CC} = 5 V,	$V_{I} = 2.5 V \text{ or } 0.5 V$		8			8		pF		

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. [‡] For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current. [§] I_{O(off)} = Power-off bus-leakage current [¶] Not more than one output should be shorted at a time and duration of the short circuit should not exceed one second.



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switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T _A = 25°C ′BCT29863B		V(C R: R: T/ SN54BC	CC = 4.5 L = 50 pF, $1 = 500 \Omega,$ $2 = 500 \Omega,$ $\Delta = \text{MIN to}$ $\Gamma 29863B$	/ to 5.5 V, MAX [†]	Г29863В	UNIT	
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
^t PLH	A	Dank	1	3	4.5	1	5.4	1	5	
^t PHL	A or B	B or A	2	4.8	6.8	2	7.9	2	7.5	ns
^t PZH		A or B	2	5.1	7	2	9.2	2	8.4	
^t PZL	OEAB or OEBA		4.5	8.4	10.8	4.5	13.6	4.5	12.6	ns
^t PHZ		A	2	5	7.2	2	9.6	2	8.8	
^t PLZ	OEAB OF OEBA A OF B		1.7	4.7	6.7	1.7	9.1	1.7	8.1	115

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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