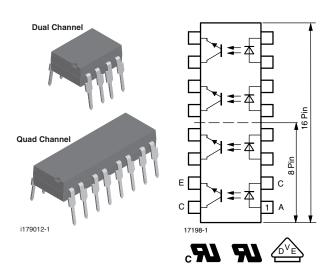


Vishay Semiconductors

Optocoupler, Phototransistor Output, (Dual, Quad Channel)



DESCRIPTION

The TCET2100/TCET4100 consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode, available in 8 pin (dual channel) and 16 pin (quad channel) package.

FEATURES

- Extra low coupling capacity typical 0.2 pF
- High common mode rejection
- Low temperature coefficient of CTR
- Rated impulse voltage (transient overvoltage)
 V_{IOTM} = 10 kV peak



RoHS

COMPLIANT

- Creepage current resistance according to VDE 0303/IEC 60112 comparative tracking index: CTI ≥ 175
- Thickness through insulation ≥ 0.4 mm
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884)
- FIMKO

ORDERING INFORMAT	2N				
ТС					
AGENCY CERTIFIED/PACKAG	CTR (%)				
UL, cUL, VDE	50 to 600				
DIP-8, dual channel	TCET2100				
DIP-16, quad channel	TCET4100				



Vishay Semiconductors Optocoupler, Phototransistor Output, (Dual, Quad Channel)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT	· ·	•		
Reverse voltage		V _R	6	V
Forward current		I _F	60	mA
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	А
Power dissipation		P _{diss}	100	mW
Junction temperature		Тj	125	°C
OUTPUT				
Collector emitter voltage		V _{CEO}	70	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		Ι _C	50	mA
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA
Power dissipation		P _{diss}	150	mW
Junction temperature		Tj	125	°C
COUPLER	· ·	•		
Isolation test voltage (RMS)	t = 1 s	V _{ISO}	5300	V _{RMS}
Isolation voltage		V _{IORM}	890	VP
Total power dissipation		P _{tot}	250	mW
Operating ambient temperature range		T _{amb}	- 55 to + 100	°C
Storage temperature range		T _{stg}	- 55 to + 150	°C
Soldering temperature ⁽²⁾	2 mm from case, t \leq 10 s	T _{sld}	260	°C

Notes

(1) Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽²⁾ Refer to wave profile for soldering conditions for through hole devices.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	$I_F = \pm 50 \text{ mA}$	V _F		1.25	1.6	V		
Junction capacitance	V _R = 0 V, f = 1 MHz	Cj		50		pF		
OUTPUT								
Collector emitter voltage	I _C = 1 mA	V _{CEO}	70			V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V		
Collector emitter cut-off current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}		10	100	nA		
COUPLER								
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}			0.3	V		
Cut-off frequency	$V_{CE} = 5 \text{ V}, \text{ I}_{\text{F}} = 10 \text{ mA}, \\ \text{R}_{\text{L}} = 100 \ \Omega$	f _c		110		kHz		
Coupling capacitance	f = 1 MHz	Ck		0.3		pF		

Note

• Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
I _C /I _F	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 5 \text{ mA}$	CTR	50		600	%	



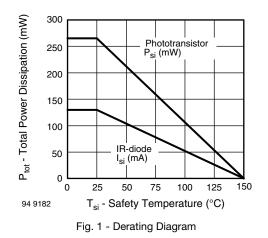
Optocoupler, Phototransistor Output, Vishay Semiconductors (Dual, Quad Channel)

MAXIMUM SAFETY RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward current		IF			275	mA		
OUTPUT								
Power dissipation		P _{diss}			400	mW		
COUPLER								
Rated impulse voltage		V _{IOTM}			10	kV		
Safety temperature		T _{si}			175	°C		

Note

• According to DIN EN 60747-5-5 (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	1.669			kV	
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$ (see figure 2)	VIOTM	10			kV	
		V _{pd}	1.424			kV	
Insulation resistance	V _{IO} = 500 V	R _{IO}	10 ¹²			Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	10 ¹¹			Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 150 \text{ °C}$ (construction test only)	R _{IO}	10 ⁹			Ω	



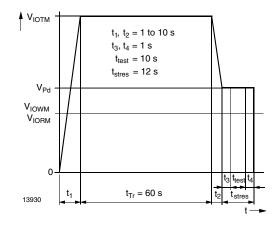
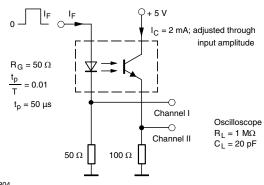


Fig. 2 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5/DIN EN 60747-; IEC60747

Vishay Semiconductors Optocoupler, Phototransistor Output, (Dual, Quad Channel)

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _d		3		μs	
Rise time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _r		3		μs	
Turn-on time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _{on}		6		μs	
Storage time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _s		0.3		μs	
Fall time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _f		4.7		μs	
Turn-off time	$\label{eq:VS} \begin{array}{l} V_{S} = 5 \; V, \; I_{C} = 2 \; mA, \; R_{L} = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _{off}		5		μs	
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 4)	t _{on}		9		μs	
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega, \\ (\text{see figure 4}) \end{array}$	t _{off}		10		μs	



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Fig. 3 - Test Circuit, Non-Saturated Operation

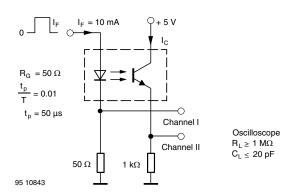


Fig. 4 - Test Circuit, Saturated Operation

 I_F 0 t p I_{C} 100 % 90 % 10 % 0 t $t_{\rm f}$ t_{off} Pulse duration Delay time $egin{aligned} t_p \ t_d \ t_r \ t_{on} \ (= t_d + t_r) \end{aligned}$ Storage time Fall time t Turn-off time Rise time $(= t_s + t_f)$ t_{off} Turn-on time 96 11698







Optocoupler, Phototransistor Output, Vishay Semiconductors (Dual, Quad Channel)

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

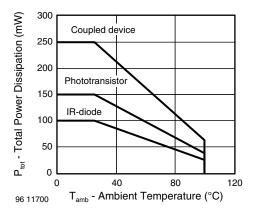


Fig. 6 - Total Power Dissipation vs. Ambient Temperature

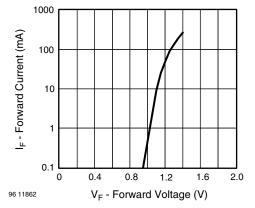


Fig. 7 - Forward Current vs. Forward Voltage

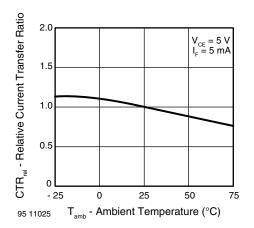


Fig. 8 - Relative Current Transfer Ratio vs. Ambient Temperature

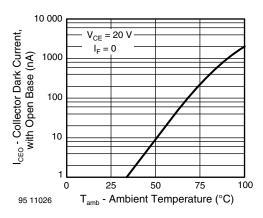


Fig. 9 - Collector Dark Current vs. Ambient Temperature

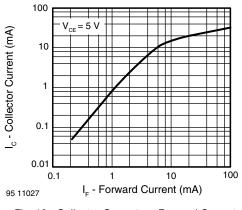


Fig. 10 - Collector Current vs. Forward Current

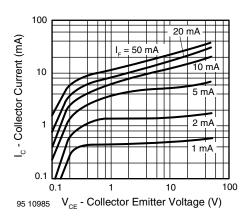


Fig. 11 - Collector Current vs. Collector Emitter Voltage

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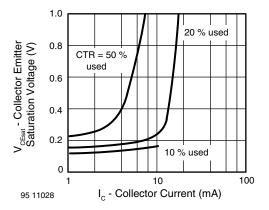


Fig. 12 - Collector Emitter Saturation Voltage vs. Collector Current

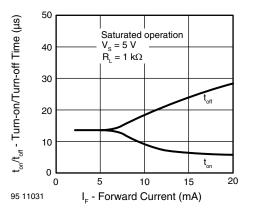


Fig. 15 - Turn-on/off Time vs. Forward Current

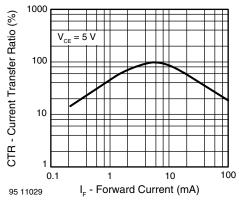


Fig. 13 - Current Transfer Ratio vs. Forward Current

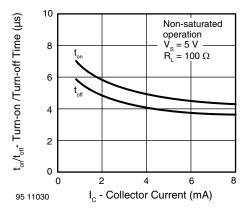


Fig. 14 - Turn-on/off Time vs. Collector Current

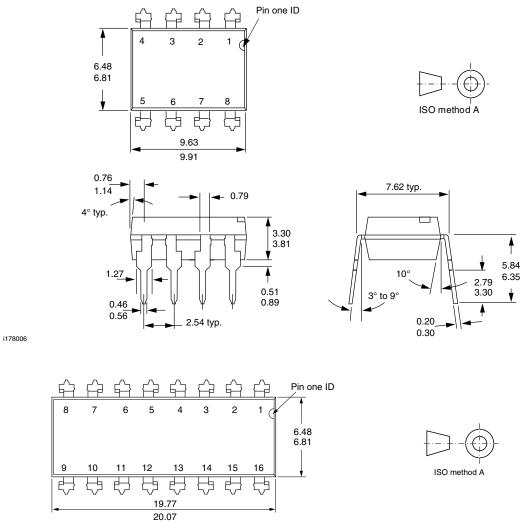


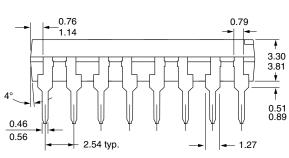
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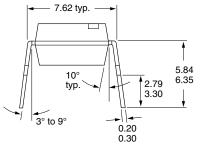
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Optocoupler, Phototransistor Output, Vishay Semiconductors (Dual, Quad Channel)

PACKAGE DIMENSIONS in millimeters

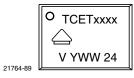






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PACKAGE MARKING





Vishay

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