Vishay Semiconductors

# High Performance Schottky Rectifier, 1 A



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SMA (DO-214AC)

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	1 A				
V <sub>R</sub>	60 V				
V <sub>F</sub> at I <sub>F</sub>	0.57 V				
I <sub>RM</sub>	7.5 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
E <sub>AS</sub>	2.0 mJ				
Package	SMA (DO-214AC)				
Circuit configuration	Single				

### FEATURES

- · Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Small footprint, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

The VS-10MQ060HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	1	A				
V <sub>RRM</sub>		60	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	40	A				
V <sub>F</sub>	1.5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.63	V				
TJ	Range	-55 to +150	C°				

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-10MQ060HM3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	60	V
Maximum working peak reverse voltage	V <sub>RWM</sub>	80	v

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS		
Maximum average forward current		50 % duty cycle at $T_L$ = 120 °C On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad are	1.5	A		
See fig. 4	IF(AV)	50 % duty cycle at $T_L$ = 129 °C On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad are				
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	40		
non-repetitive surge current See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	10	A	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 4 mH		2.0	mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical 1.0		1.0	А	

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
	V (1)	1 A	т ог «О	0.63	V	
Maximum forward voltage drop See fig. 1		1.5 A	T <sub>J</sub> = 25 °C	0.71		
	V <sub>FM</sub> <sup>(1)</sup>	1 A	T.I = 125 °C	0.57		
		1.5 A	$1_{\rm J} = 125$ C	0.63		
Maximum reverse leakage current		T <sub>J</sub> = 25 °C	V - Reted V	0.5	mA	
See fig. 2	I <sub>RM</sub>	$T_J = 125 \text{ °C}$ $V_R = \text{Rated } V_R$		7.5	ША	
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.45	V	
Forward slope resistance	r <sub>t</sub>			86.8	mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}$ , $T_J = 25 \text{ °C}$ , test signal = 1 MHz		31	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $^{(1)}\,$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	$T_{J}$ <sup>(1)</sup> , $T_{Stg}$		-55 to +150	°C	
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W	
Annyovingete weight		0.07	g		
Approximate weight			0.002	oz.	
Marking device		Case style SMA (DO-214AC)	11	4	

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

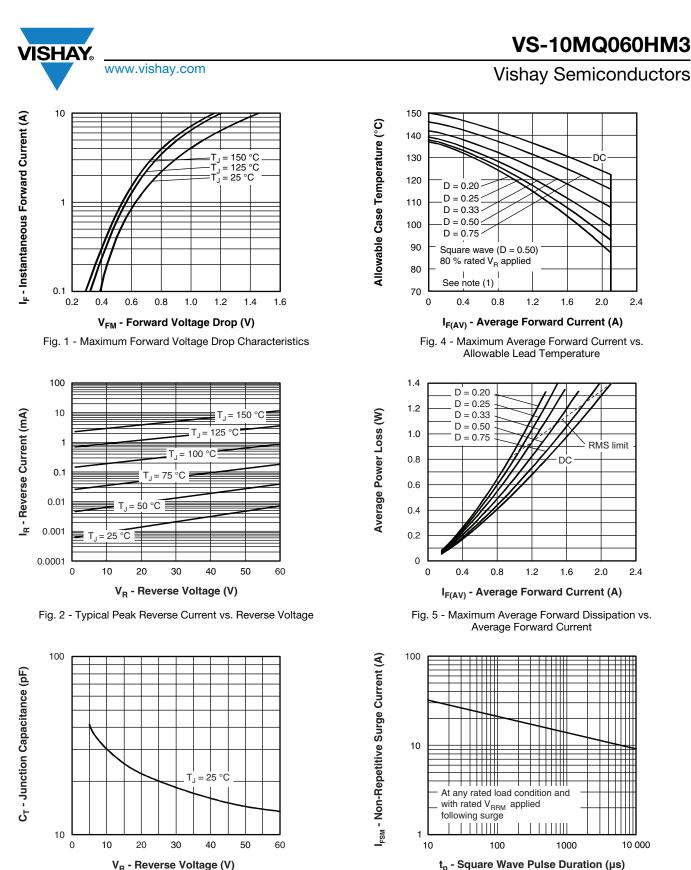




Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

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## **ORDERING INFORMATION TABLE**

Device and		40		•			
Device code	VS-	10	M	Q	060	н	M3
	1	2	3	4	5	6	7
	1	- Visl	hay Sem	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng			
	3	- M=	SMA				
	4	- Q =	Schottk	ky "Q" se	eries		
	5	- Vol	tage rati	ng (060	= 60 V)	)	
	6	- H=	AEC-Q	101 qua	alified		
	7	- Env	vironmer	ntal digit			
		М3	= Halog	en-free,	RoHS-	complia	int and t

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-10MQ060HM3/5AT	5AT	7500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95400				
Part marking information	www.vishay.com/doc?95403				
Packaging information	www.vishay.com/doc?95404				



# **Outline Dimensions**

# **Vishay Semiconductors**

SMA

## **DIMENSIONS** in inches (millimeters)

DO-214AC (SMA)





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