- · Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- · Small foot print, surface mountable
- · High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-12CWQ06FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	12	А		
V _{RRM}		60	V		
I _{FSM}	t _p = 5 μs sine	320	А		
V _F	$6 A_{pk}, T_J = 125 \ ^{\circ}C \ (per \ leg)$	0.57	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-12CWQ06FNHM3	UNITS			
Maximum DC reverse voltage	V _R	60	N/			
Maximum working peak reverse voltage V _{RWM}		00	V			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum averageper legforward current			50 % duty cycle at T _C = 131 °C, rectangular waveform –		6	A
		I _{F(AV)}			12	
Maximum peak one cycle non-repetitive surge current See fig. 7		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	320	A
			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	105	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1.2 A, L = 10 mH		7	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		0.8	А

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1

Schottky Rectifier, 2 x 6 A

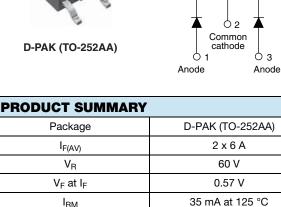
Base common cathode

 $\cap 4$

150 °C

Common cathode

7 mJ



 I_{RM}

T_J max.

Diode variation

E_{AS}

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RoHS COMPLIANT HALOGEN FREE

VS-12CWQ06FNHM3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CC	NDITIONS	VALUES	UNITS	
	V _{FM} ⁽¹⁾	6 A	– T _{.1} = 25 °C	0.61	V	
Maximum forward voltage drop per leg		12 A	1j=25 0	0.79		
See fig. 1		6 A	– T _{.1} = 125 °C	0.57		
		12 A	1j = 125 C	0.72		
Maximum reverse	ı (1)	T _J = 25 °C		3		
See fig. 2	eakage current per leg I _{RM} ⁽¹⁾		 V_R = Rated V_R 	35	mA	
Threshold voltage	V _{F(TO)}	·		0.36	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		24.14	mΩ	
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		360	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0			nH	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

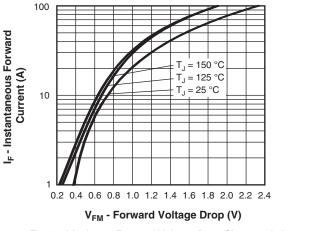
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_{J} ⁽¹⁾ , T_{Stg}		- 55 to 150	°C
Maximum thermal resistance,	per leg	P	DC operation	3.0	°C/W
junction to case	per device	R _{thJC}	See fig. 4	1.5	0/ 10
Approximate weight				0.3	g
				0.01	oz.
Marking device			Case style D-PAK	12CWC	06FNH

Note

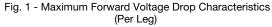
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

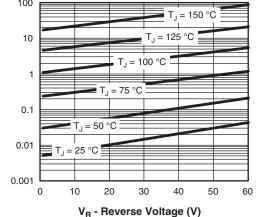
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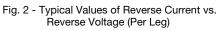
I_R - Reverse Current (mA)



SHA







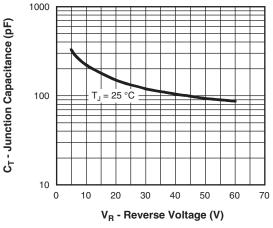


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

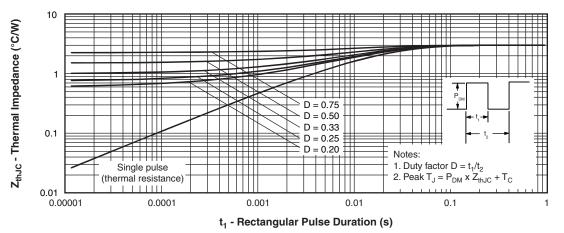
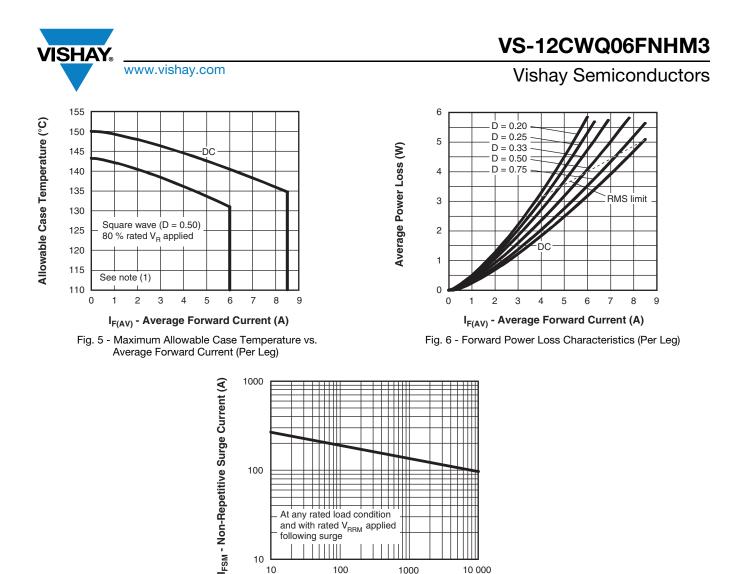


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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t_p - Square Wave Pulse Duration (μs)

1000

10 000

100

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

10

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

VS-12CWQ06FNHM3

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ORDERING INFORMATION TABLE

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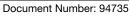
Device code	VS-	12	С	w	Q	06	FN	TRL	н	М3
	1	2	3	4	5	6	7	8	9	(10)
	1	- Visl	nay Sen	niconduc	ctors pro	oduct				
	2	- Cur	rent rati	ng (12 A	A)					
	3	- Cer	nter tap	configur	ation					
	4	- Pac	kage id	entifier:						
	_	W =	D-PAK							
	5	- Sch	ottky "C	" series						
6 - Voltage rating (06 = 60 V)										
	7 - FN = TO-252AA									
	8	• N	one = T	ube						
		• TI	R = Tap	e and re	el					
	 TRL = Tape and reel (left oriented) 									
	pe and i	reel (rig	nt orient	ted)						
	9	- H=	AEC-Q	101 qua	alified					
	10	- Env	rironmer	ntal digit	:					
	M3 = Halogen-free, RoHS-compliant, and terminations lead					ad (Pb)-fr				

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-12CWQ06FNHM3	75	3000	Antistatic plastic tube			
VS-12CWQ06FNTRHM3	2000	2000	13" diameter reel			
VS-12CWQ06FNTRRHM3	3000	3000	13" diameter reel			
VS-12CWQ06FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95519		
Part marking information	www.vishay.com/doc?95518		
Packaging information	www.vishay.com/doc?95033		

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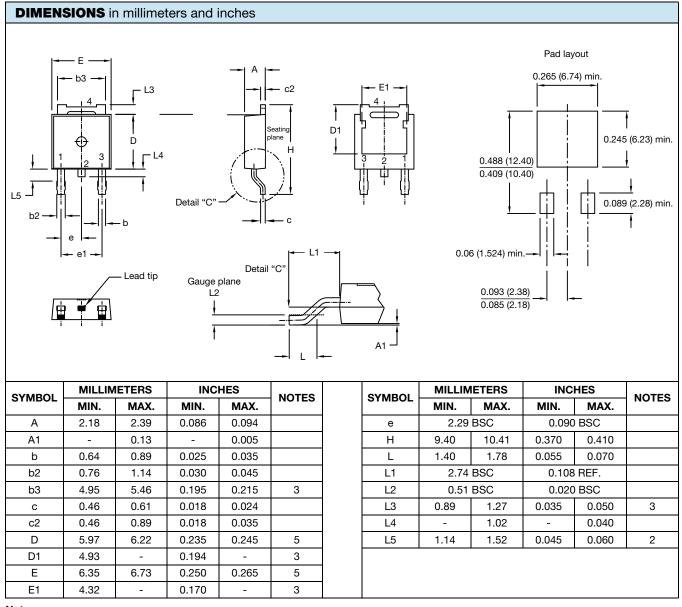


Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA, except for D1 dimension



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