Vishay Semiconductors

Hyperfast Rectifier, 3 A FRED Pt[®]



www.vishay.com



SMA (DO-214AC)

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	3 A			
V _R	600 V			
V _F at I _F	1.2 V			
t _{rr} typ.	35 ns			
T _J max.	175 °C			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

FEATURES

- Hyperfast recovery time, reduced Q_{rr} and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM/CCM, snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMA (DO-214AC)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V _{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	$T_L = 81 \ ^{\circ}C \ ^{(1)}$	3	- A	
Non-repetitive peak surge current	I _{FSM}	$T_J = 25 \ ^{\circ}C, 6 \ ms \ square \ pulse$	50		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

Note

⁽¹⁾ Mounted on PCB with minimum pad size

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	
Forward voltage	V _F	I _F = 3 A	-	1.4	1.7	V
		I _F = 3 A, T _J = 150 °C	-	1.20	1.35	
Reverse leakage current	I _R	V _R = V _R rated	-	-	3	
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	100	μA
Junction capacitance	CT	V _R = 600 V	-	3.7	-	pF

Revision: 23-Feb-2021

1

Document Number: 94776

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



COMPLIANT



www.vishay.com

Vishay Semiconductors

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \ ^{\circ}C$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
			$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		35	-	
			$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		40	-	
Reverse recovery time	t _{rr}	I _F = 0.5 A, I _R = 1 A, I	_r = 0.25 Α	-	-	45	ns
		T _J = 25 °C		-	25	-	
		T _J = 125 °C		-	36	-	
Peak resource ourrent	ak recovery current I _{RRM}	T _J = 25 °C	I _F = 3 A dI _F /dt = 200 A/μs	-	3.9	-	А
Feak recovery current		T _J = 125 °C	$V_{\rm R} = 390 \text{V}$	-	5.3	-	A
	Reverse recovery charge Q _{rr}	T _J = 25 °C	••	-	50	-	nC
neverse recovery charge		T _J = 125 °C		-	98	-	пС

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to mount	R _{thJM} ⁽¹⁾		-	-	20	°C/W
Thermal resistance, junction to ambient	R _{thJA} ⁽¹⁾		-	-	95	0/11
Approving to Maight				0.07		g
Approximate Weight				0.002		oz.
Marking device		Case style SMA (DO-214AC)		31	16	

Note

 $^{(1)}$ Mounted on PCB with minimum pad size

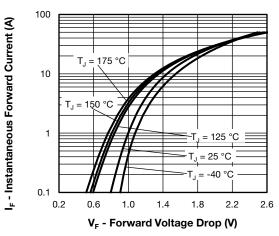


Fig. 1 - Typical Forward Voltage Drop Characteristics

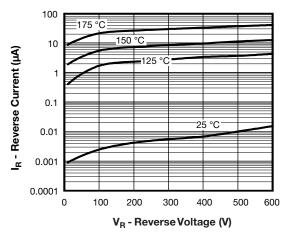
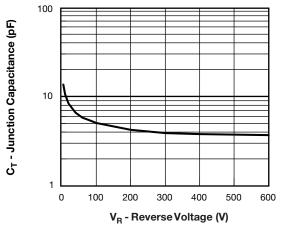


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

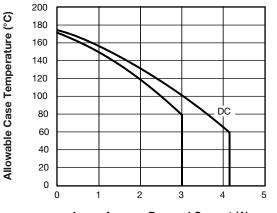


Vishay Semiconductors



www.vishay.com

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



I_{F(AV)} - Average Forward Current (A)

Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

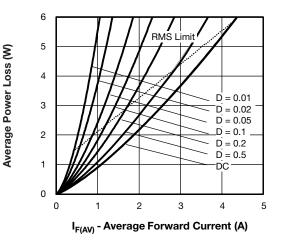
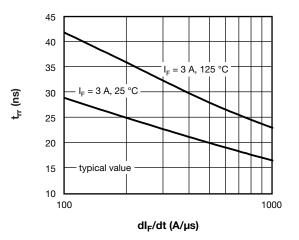
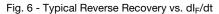


Fig. 5 - Forward Power Loss Characteristics





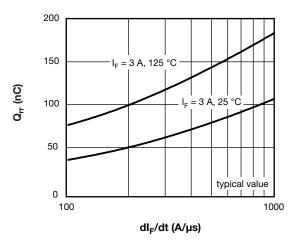


Fig. 7 - Typical Stored Charge vs. dl_F/dt

 Revision: 23-Feb-2021
 3
 Document Number: 94776

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

VS-3EMH06-M3

Vishay Semiconductors

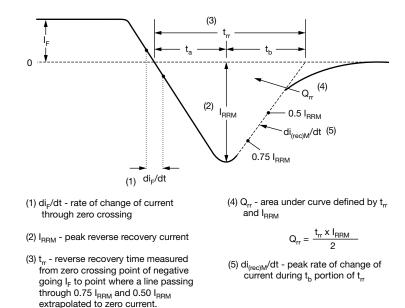
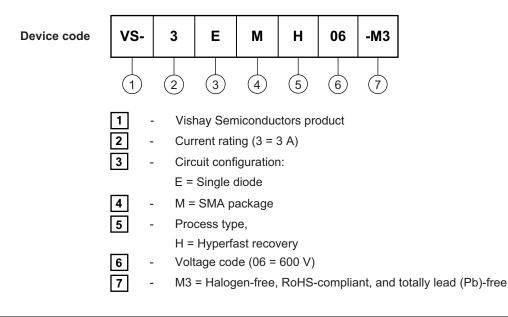


Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

www.vishay.com



ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-3EMH06-M3/5AT	7500	7500	13"diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95472			
Packaging information	www.vishay.com/doc?95404			

Revision: 23-Feb-2021

4

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



Outline Dimensions

Vishay Semiconductors

SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)





Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.