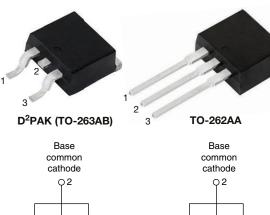
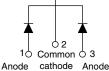
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SHAY

Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 20 A





റ്റ 10 Common 👌 3 Anode cathode Anode

VS-47CTQ020S-M3

VS-47CTQ020-1-M3

PRIMARY CHARACTERISTICS							
I _{F(AV)}	2 x 20 A						
V _R	20 V						
V _F at I _F	0.34 V						
I _{RM} max.	310 mA at 125 °C						
T _J max.	150 °C						
E _{AS}	18 mJ						
Package	D ² PAK (TO-263AB), TO-262AA						
Circuit configuration	Common cathode						

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I _{F(AV)}	Rectangular waveform	40	А							
V _{RRM}		20	V							
I _{FSM}	t _p = 5 μs sine	1000	A							
V _F	20 A _{pk} , T _J = 125 °C	0.34	V							
TJ		-55 to +150	°C							

VOLTAGE RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VS-47CTQ020S-M3 VS-47CTQ020-1-M3	UNITS				
Maximum DC reverse voltage		125 °C	20	V				
	V _R	150 °C	10	v				

Revision: 21-Dec-2021

Document Number: 94943

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HALOGEN FREE



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ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS						
Maximum average per leg		50 % duty cycle at T _C = 135 °C	20						
forward current per device	I _{F(AV)}	50% duty cycle at $T_C = 155\%$	40						
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load	1000	А				
non-repetitive surge current per leg		10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	250					
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 3 mH		18	mJ				
Repetitive avalanche current per leg		Current decaying linearly to zer Frequency limited by T _J maxim	•	3	А				

ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST	CONDITIONS	VALUES	UNITS					
		20 A	T 05 %C	0.45						
		40 A	—— T _J = 25 °C	0.51						
Maximum forward valtage drep per leg	V (1)	20 A	T.I = 125 °C	0.34	V					
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A		0.44	V					
		20 A	T 150 %C	0.31						
		40 A	—— T _J = 150 °C	0.42						
		T _J = 125 °C	V _R = 5 V	60						
Martin and a statistical			V _R = 3.3 V	45	mA					
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 150 °C	V _R = 10 V	306						
current per leg		T _J = 25 °C		3						
		T _J = 125 °C	V _R = Rated V _R	310						
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.188	V					
Forward slope resistance	r _t			5.9	mΩ					
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		3000	pF					
Typical series inductance per leg	L _S	Measured lead to lead	5.5	nH						
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C					
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package Typical thermal resistance, case to heatsink		D	DC operation	1.5						
		R _{thJC}	DC operation	0.75	°C/W					
		R _{thCS} Mounting surface, smooth and greased		0.50						
Approximate weight				2	g					
Approximate weight				0.07	oz.					
Mounting torque	minimum			6 (5)	kgf ⋅ cm					
Mounting torque -	maximum			12 (10)	(lbf · in)					
Marking device			Case style D ² PAK (TO-263AB)	47CTQ)20S					
			Case style TO-262AA	47CTQ0	20-1					

Revision: 21-Dec-2021 For technical questions within Document Number: 94943

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VS-47CTQ020S-M3, VS-47CTQ020-1-M3

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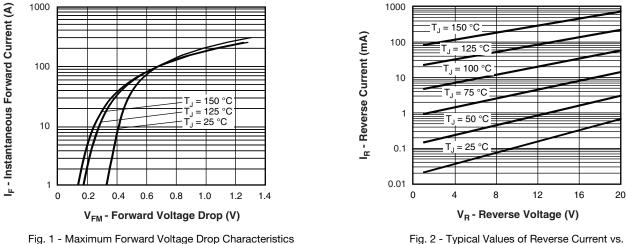




Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

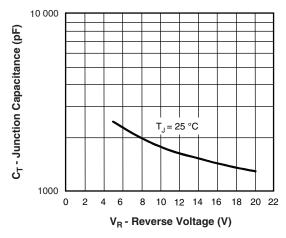
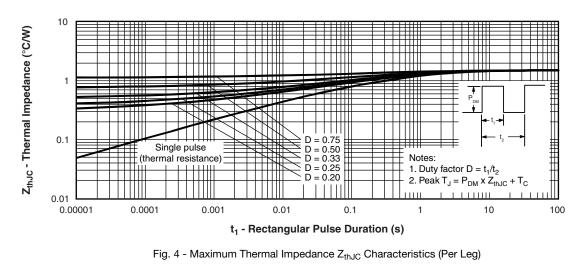


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

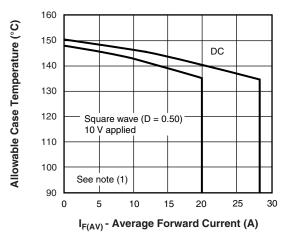


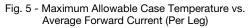
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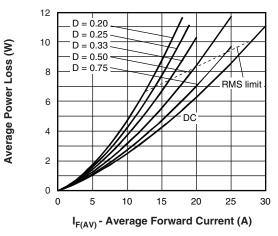


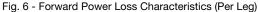
VS-47CTQ020S-M3, VS-47CTQ020-1-M3

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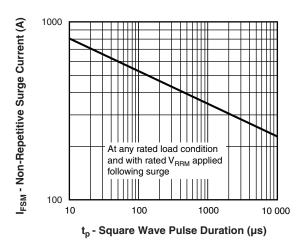


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

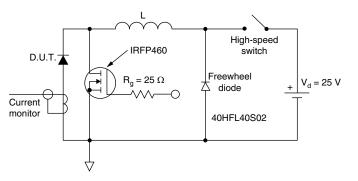


Fig. 8 - Unclamped Inductive Test Circuit

Note

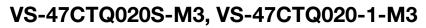
- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mbox{Pd} = \mbox{forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/D) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{ 10 V} \end{array}$

Revision: 21-Dec-2021

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Vishay Semiconductors

ORDERING INFORMATION TABLE

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SHAY

Device code	vs-	47	С	т	Q	020	s	TRL	-МЗ
		2	3	4	5	6	7	8	9
	1 · · · · · · · · · · · · · · · · · · ·	- Cur - Circ - T = - Sch - Volt	rent rati cuit conf TO-220 nottky "C tage rati	niconduc ng (40 <i>A</i> ïguration) 2° series ing (020 .K (TO-2	A) n: C = c = 20 V)	ommon	cathod	e	
	8.	• -1	= TO-2 one = tu	62AA	,				
	9	• TI	RR = ta	be and ro pe and r gen-free	eel (righ	nt orient	ed - for	D ² PAK	(TO-26

ORDERING INFORMATION									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-47CTQ020S-M3	50	Antistatic plastic tubes							
VS-47CTQ020STRL-M3	800	13" diameter plastic tape and reel							
VS-47CTQ020STRR-M3	800	13" diameter plastic tape and reel							
VS-47CTQ020-1-M3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS								
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164						
	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444						
	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

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Outline Dimensions



Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

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