AUTOMOTIVE

COMPLIANT

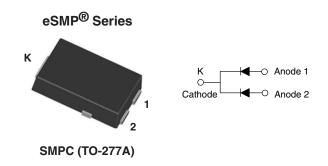
**HALOGEN** 

FREE



### Vishay Semiconductors

## Hyperfast Rectifier, 2 x 2 A FRED Pt®



#### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 2 A			
V <sub>R</sub>	200 V			
V <sub>F</sub> at I <sub>F</sub>	0.75 V			
t <sub>rr (typ.)</sub>	24 ns			
T <sub>J</sub> max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Hyperfast recovery time, reduced Q<sub>rr</sub>, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

#### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		$V_{RRM}$		200	V	
Average restified forward comment	per device	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 165 °C	4		
Average rectified forward current	per diode			2	_	
Non repetitive peak aurae aurrent	per device	I <sub>FSM</sub>	T = 25 °C	90	A	
Non-repetitive peak surge current	per diode		T <sub>J</sub> = 25 °C	50		
Operating junction and storage terr	nperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	200	-	-	
Converd voltage neg diede		I <sub>F</sub> = 2 A	-	0.88	0.95	V
Forward voltage, per diode	V <sub>F</sub>	I <sub>F</sub> = 2 A, T <sub>J</sub> = 125 °C	-	0.75	0.82	
Reverse leakage current, per diode	I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	-	2	
neverse leakage current, per diode		$T_J = 125 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	1	8	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	8	-	pF

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A, } dI_F/dt =$	$= 50 \text{ A/}\mu\text{s}, V_{\text{R}} = 30 \text{ V}$	ı	24	-	
Boyerse receivent time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		ı	ı	25	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	16	-	ns
		T <sub>J</sub> = 125 °C		-	22	-	
Dook recovery ourrent	I	T <sub>J</sub> = 25 °C	$I_F = 2 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	2	-	Α
Peak recovery current	IRRM	T <sub>J</sub> = 125 °C		-	3	-	
Povorco rocovoru chargo	Reverse recovery charge Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		ı	16	-	nC
heverse recovery charge		T <sub>J</sub> = 125 °C		-	30	-	iic

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to mount, per diode	R <sub>thJM</sub>		-	4.5	5.5	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		JC	H2	

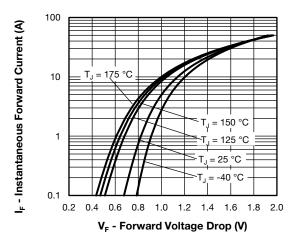


Fig. 1 - Typical Forward Voltage Drop Characteristics

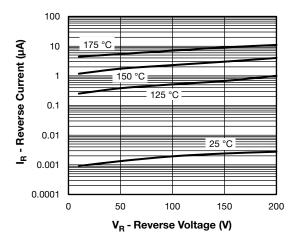


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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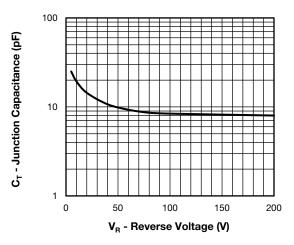


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

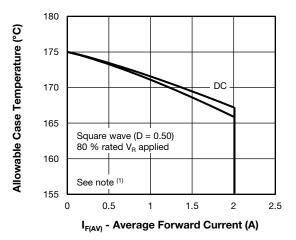


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

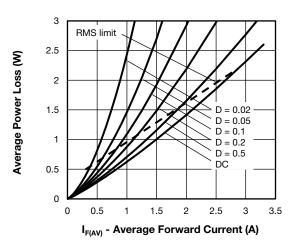


Fig. 5 - Forward Power Loss Characteristics

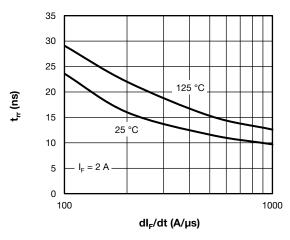


Fig. 6 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

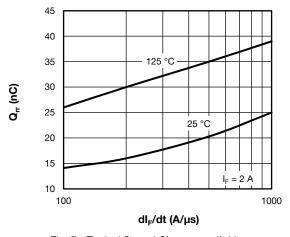
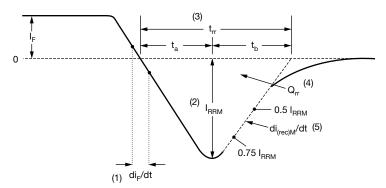


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $<sup>\</sup>begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$ 

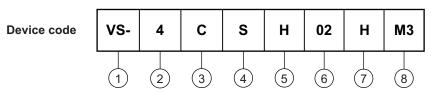
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- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (4)  $\boldsymbol{Q}_{rr}$  area under curve defined by  $\boldsymbol{t}_{rr}$  and  $\boldsymbol{I}_{RRM}$
- (2) I<sub>RRM</sub> peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (5)  $di_{(rec)M}/dt$  peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 8 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**



- 1 Vishay Semiconductors product
- Current rating (4 = 4 A)
- 3 Circuit configuration:
  - C = common cathode
- S = SMPC package
- **5** Process type,
  - H = hyper fast recovery
- 6 Voltage code (02 = 200 V)
- 7 H = AEC-Q101 qualified
- 8 M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-4CSH02HM3/86A	1500	1500	7" diameter plastic tape and reel			
VS-4CSH02HM3/87A	6500	6500	13" diameter plastic tape and reel			

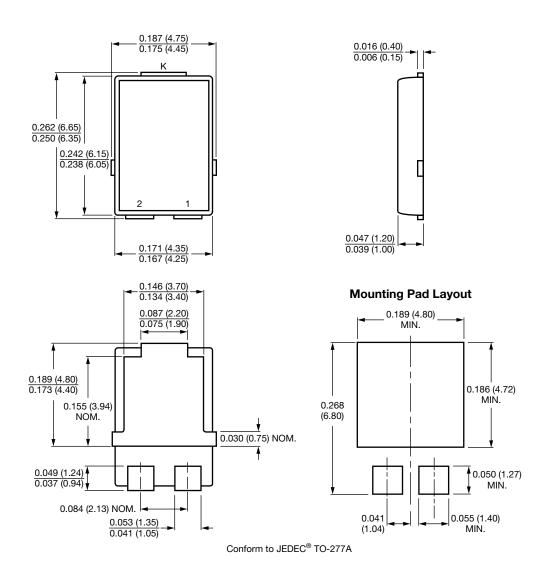
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95570</u>				
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			



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# **SMPC (TO-277A)**

### **DIMENSIONS** in inches (millimeters)





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