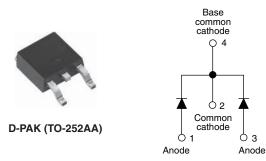
**Vishay Semiconductors** 



# Schottky Rectifier, 2 x 3.5 A



PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I <sub>F(AV)</sub>	2 x 3.5 A				
V <sub>R</sub>	100 V				
V <sub>F</sub> at I <sub>F</sub>	See Electrical table				
I <sub>RM</sub>	4.9 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	5 mJ				

### FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-6CWQ10FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UI					
I <sub>F(AV)</sub>	Rectangular waveform	7	A				
V <sub>RRM</sub>		100	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	440	A				
V <sub>F</sub>	$3 A_{pk}, T_J = 125 \ ^{\circ}C \ (per \ leg)$	0.63	V				
TJ	Range	- 40 to 150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-6CWQ10FNHM3	UNITS			
Maximum DC reverse voltage	V <sub>R</sub>	100	V			
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average per leg		50% duty cycle at T <sub>2</sub> = 125% rootangular waveform		3.5		
See fig. 5 per device	$I_{F(AV)}$ 50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		7	٨		
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	440	A	
non-repetitive surge current per leg See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	70		
Non-repetitive avalanche energy per leg E <sub>AS</sub>		T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 10 mH		5.0	mJ	
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.5	А	

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e3 RoHS





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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		3 A	T <sub>.1</sub> = 25 °C	0.81	V	
Maximum forward	V <sub>FM</sub> <sup>(1)</sup>	6 A	1j=25 C	0.96		
voltage drop per leg See fig. 1	VFM ()	3 A	T 105 %O	0.63		
		6 A	T <sub>J</sub> = 125 °C	0.74		
Maximum reverse	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1	mA	
leakage current per leg See fig. 2	IRM ("	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	4.9		
Threshold voltage	V <sub>F(TO)</sub>	T T maximum				
Forward slope resistance	r <sub>t</sub>	$I_{J} = I_{J} maximum$	$T_J = T_J$ maximum			
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C 92			pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.0 nH			nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs			V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		$T_{J}$ <sup>(1)</sup> , $T_{Stg}$		- 40 to 150	°C
Maximum thermal resistance,	per leg	R <sub>thJC</sub>	DC operation	4.70	°C/W
junction to case	per device	nthJC	See fig. 4	2.35	0/10
Approvimete weight				0.3	g
Approximate weight				0.01	oz.
Marking device			Case style D-PAK	6CWQ	10FNH

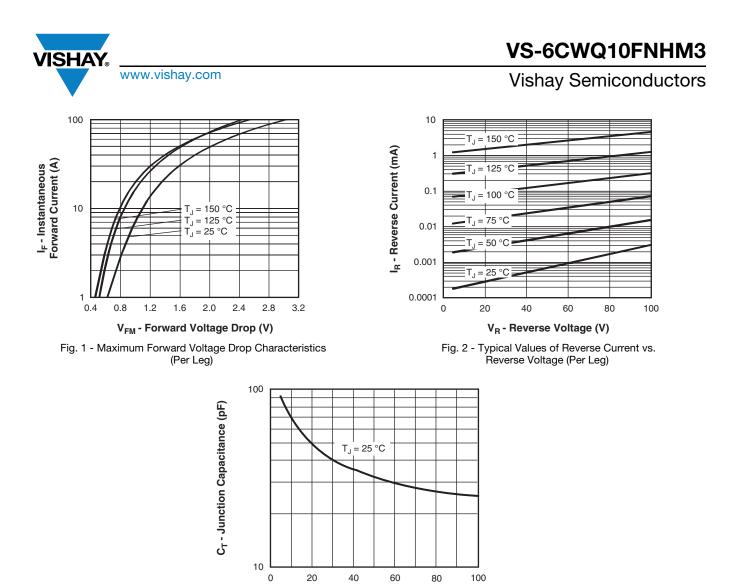
### Note

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

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V<sub>R</sub> - Reverse Voltage (V) Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

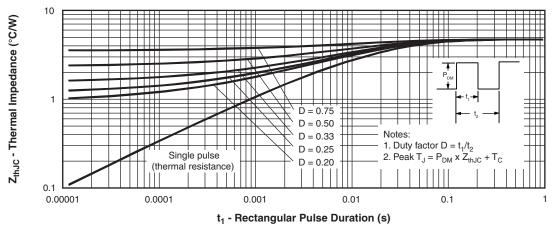


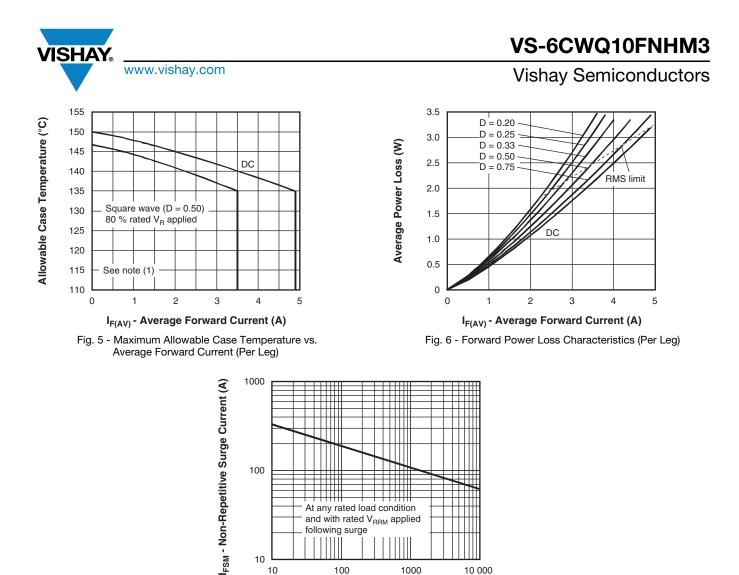
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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t<sub>p</sub> - Square Wave Pulse Duration (μs)

1000

10 000

100

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

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<sup>&</sup>lt;sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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### **ORDERING INFORMATION TABLE**

www.vishay.com

SHA'

Device code	VS-	6	С	w	Q	10	FN	TRL	н	M3
	1	2	3	4	5	6	7	8	9	10
	1	- Vis	hay Sen	nicondu	ctors pro	oduct				
	2	- Cui	rrent rati	ng (7 A)	)					
	3	- Cei	nter tap	configur	ation					
	4	- Pao	ckage id	entifier:						
		W =	= D-PAK	ζ.						
	5	- Scł	nottky "C	)" series						
	6	- Vol	tage rati	ing (10 =	= 100 V)	)				
	7	- FN	= TO-2	52AA						
	8	- • N	one = T	ube						
		• T	R = Tap	e and re	el					
		• T	RL = Ta	pe and r	eel (left	oriente	d)			
		• T	RR = Ta	pe and	reel (rig	ht orien	ted)			
	9	- H=	AEC-Q	101 qua	alified					
	10	- Env	vironme	ntal digit						
		1/2		ion froo	DALIC	oomolio	nt and	tormino	tionala	

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-6CWQ10FNHM3	75	3000	Antistatic plastic tube			
VS-6CWQ10FNTRHM3	2000	2000	13" diameter reel			
VS-6CWQ10FNTRRHM3	3000	3000	13" diameter reel			
VS-6CWQ10FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

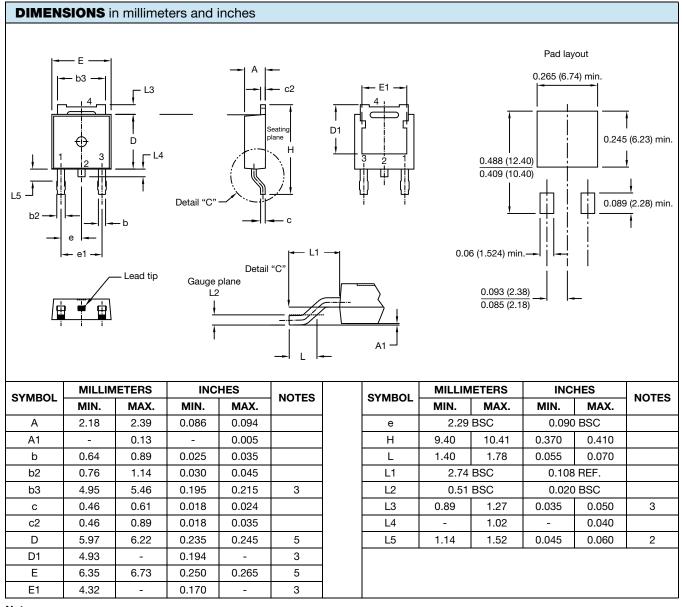
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## **Outline Dimensions**



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# DPAK (TO-252AA)



#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension uncontrolled in L5

<sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(5)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-252AA, except for D1 dimension



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