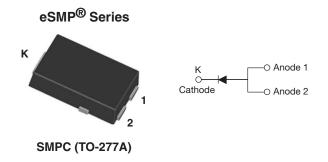


## Hyperfast Rectifier, 6 A FRED Pt®



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	6 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.05 V			
t <sub>rr (typ.)</sub>	33 ns			
T <sub>J</sub> max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

#### **FEATURES**

 Hyperfast recovery time, reduced Q<sub>rr</sub>, and soft recovery



• 175 °C maximum operating junction temperature

• For PFC, CRM/CCM, snubber operation

HALOGEN FREE

- Low forward voltage drop
- Low for ward vortage an
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

#### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 145 °C	6	۸
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	90	А
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 6 A	-	1.30	1.80	V
		I <sub>F</sub> = 6 A, T <sub>J</sub> = 150 °C	-	1.05	1.55	
Reverse leakage current I <sub>R</sub>	$V_R = V_R$ rated	-	-	5		
	I'R	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	50	300	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	8	-	pF



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt$	$= 50 \text{ A/}\mu\text{s}, V_{\text{R}} = 30 \text{ V}$	-	33	-	
Boyorna raggyany tima	+	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		-	-	40	1
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	40	-	ns
		T <sub>J</sub> = 125 °C	-	75	-		
Peak recovery current I <sub>RRN</sub>	1	T <sub>J</sub> = 25 °C	$I_F = 6 \text{ A}$ $dI_F/dt = 500 \text{ A/}\mu\text{s}$ $V_R = 400 \text{ V}$	-	6.8	=	А
	IRRM	T <sub>J</sub> = 125 °C		-	11	-	
Reverse recovery charge Q <sub>r</sub>	0	T <sub>J</sub> = 25 °C		-	140	=	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	400	=	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-65	-	175	°C
Thermal resistance, junction to mount	$R_{thJM}$		-	2.4	3.5	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		NE	H6	

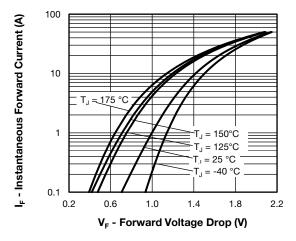


Fig. 1 - Typical Forward Voltage Drop Characteristics

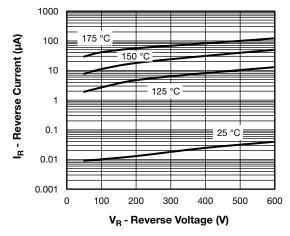


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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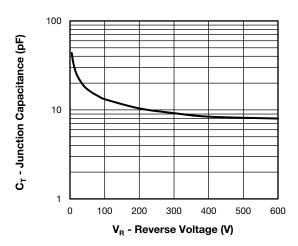


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

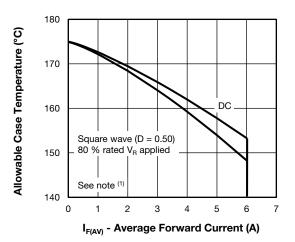


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

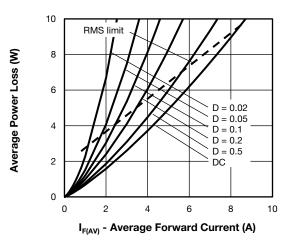


Fig. 5 - Forward Power Loss Characteristics

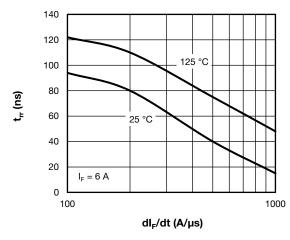


Fig. 6 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

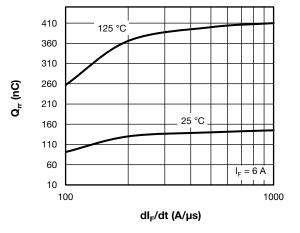
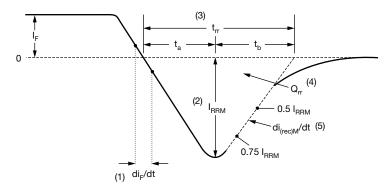


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

### Note

 $<sup>\</sup>begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$ 



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

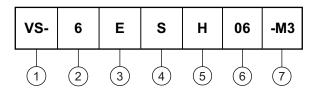
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 8 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

Device code



- Vishay Semiconductors product
- 2 Current rating (6 = 6 A)
- Circuit configuration:

E = single diode

4 - S = SMPC package

5 - Process type,

H = hyper fast recovery

6 - Voltage code (06 = 600 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

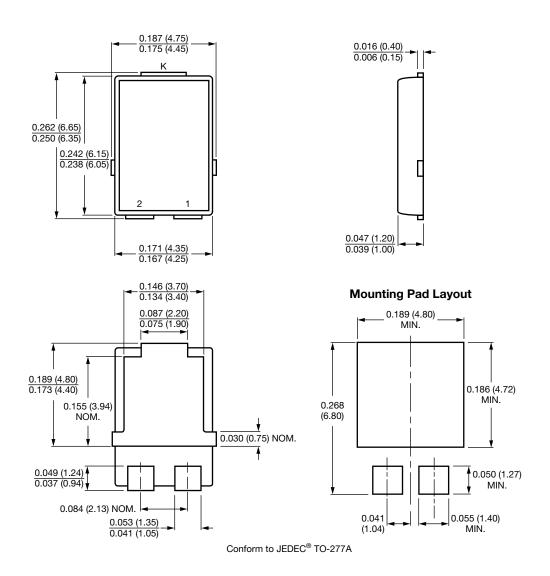
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-6ESH06-M3/86A	1500	1500	7" diameter plastic tape and reel		
VS-6ESH06-M3/87A	6500	6500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95570
Part marking information	www.vishay.com/doc?95565
Packaging information	www.vishay.com/doc?88869



# **SMPC (TO-277A)**

### **DIMENSIONS** in inches (millimeters)





### **Legal Disclaimer Notice**

Vishay

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