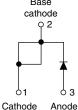


Ultrafast Rectifier, 8 A FRED Pt®





TO-220AC

Base

PRODUCT SUMMARY					
Package	TO-220AC				
I _{F(AV)}	8 A				
V_{R}	400 V				
V _F at I _F	0.94 V				
t _{rr} typ.	See Recovery table				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- AEC-Q101 qualified, meets JESD 201, class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





HALOGEN **FREE**

DESCRIPTION / APPLICATIONS

FRED Pt® series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Repetitive peak reverse voltage	V_{RRM}		400	V			
Average rectified forward current	I _{F(AV)}	T _C = 155 °C	8				
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	100	Α			
Repetitive peak forward current	I _{FRM}		16				
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	400	-	-		
Forward voltage	V	I _F = 8 A	-	1.19	1.3	V	
	V _F	I _F = 8 A, T _J = 150 °C	-	0.94	1.0		
Reverse leakage current I _R		V _R = V _R rated	-	0.2	10		
		T _J = 150 °C, V _R = V _R rated	-	20	500	μA	
Junction capacitance	C _T	V _R = 400 V	-	14	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 A$, $dI_F/dt =$	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		35	60		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	43	-	ns	
		T _J = 125 °C	$I_F = 8 A$ $dI_F/dt = 200 A/\mu s$ $V_R = 200 V$	-	67	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.8	-	А	
		T _J = 125 °C		-	6.3	-	Α	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	60	-	nC	
		T _J = 125 °C		-	210	-	IIC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance, junction to case	R _{thJC}		-	1.8	2		
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	50	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	2.0	-	g	
vveignt			-	0.07	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-220AC	8ETU04H				

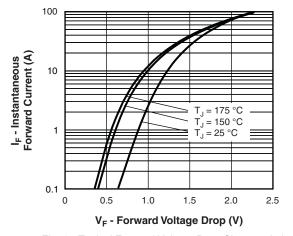


Fig. 1 - Typical Forward Voltage Drop Characteristics

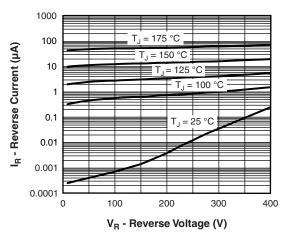


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

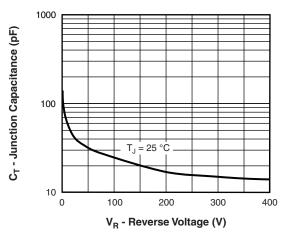


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

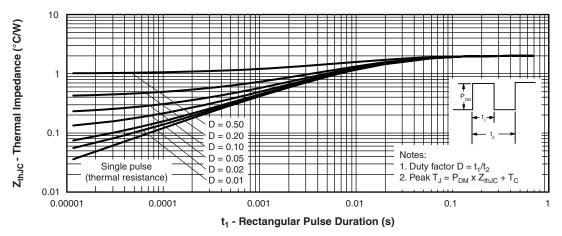


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

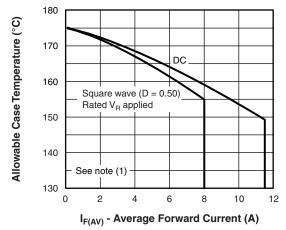


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

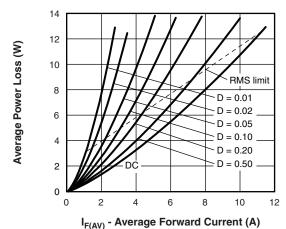


Fig. 6 - Forward Power Loss Characteristics

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times R_{\text{th,JC}}; \\ \text{Pd} & = \text{Forward power loss} = I_{\text{F(AV)}} \times V_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} & = \text{Inverse power loss} = V_{\text{R1}} \times I_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } V_{\text{R1}} = \text{Rated } V_{\text{R}} \\ \end{array}$

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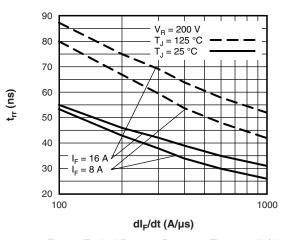


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

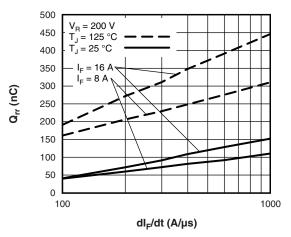
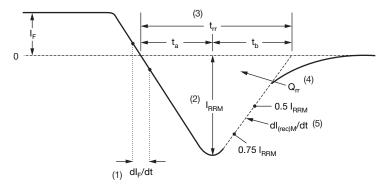


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RBM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

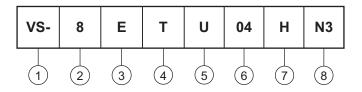
(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (8 = 8 A)
- 3 E = single diode
- 4 Package:
 - T = TO-220
- 5 U = ultrafast recovery
- 6 Voltage rating (04 = 400 V)
- 7 H = AEC-Q101 qualified
- 8 Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

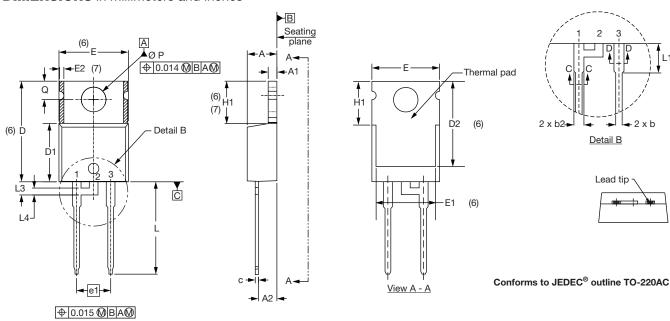
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-8ETU04HN3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95221				
Part marking information	www.vishay.com/doc?95068				
SPICE model	www.vishay.com/doc?95441				



TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS INCHES		HES	NOTES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.25	4.65	0.167	0.183		
A1	1.14	1.40	0.045	0.055		
A2	2.56	2.92	0.101	0.115		
b	0.69	1.01	0.027	0.040		
b1	0.38	0.97	0.015	0.038	4	
b2	1.20	1.73	0.047	0.068		
b3	1.14	1.73	0.045	0.068	4	
С	0.36	0.61	0.014	0.024		
c1	0.36	0.56	0.014	0.022	4	
D	14.85	15.25	0.585	0.600	3	
D1	8.38	9.02	0.330	0.355		
D2	11.68	12.88	0.460	0.507	6	
E	10.11	10.51	0.398	0.414	3, 6	

MILLIMETERS INCHES		NOTES		
MIN.	MAX.	MIN.	MAX.	NOTES
6.86	8.89	0.270	0.350	6
-	0.76	-	0.030	7
4.88	5.28	0.192	0.208	
5.84	6.86	0.230	0.270	6, 7
13.52	14.02	0.532	0.552	
3.32	3.82	0.131	0.150	2
1.78	2.13	0.070	0.084	
0.76	1.27	0.030	0.050	2
3.54	3.73	0.139	0.147	
2.60	3.00	0.102	0.118	
	MIN. 6.86 - 4.88 5.84 13.52 3.32 1.78 0.76 3.54	MIN. MAX. 6.86 8.89 - 0.76 4.88 5.28 5.84 6.86 13.52 14.02 3.32 3.82 1.78 2.13 0.76 1.27 3.54 3.73	MIN. MAX. MIN. 6.86 8.89 0.270 - 0.76 - 4.88 5.28 0.192 5.84 6.86 0.230 13.52 14.02 0.532 3.32 3.82 0.131 1.78 2.13 0.070 0.76 1.27 0.030 3.54 3.73 0.139	MIN. MAX. MIN. MAX. 6.86 8.89 0.270 0.350 - 0.76 - 0.030 4.88 5.28 0.192 0.208 5.84 6.86 0.230 0.270 13.52 14.02 0.532 0.552 3.32 3.82 0.131 0.150 1.78 2.13 0.070 0.084 0.76 1.27 0.030 0.050 3.54 3.73 0.139 0.147

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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Vishay

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