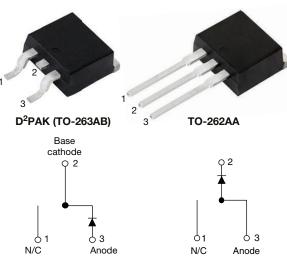
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# VS-ETH3006S-M3, VS-ETH3006-1-M3

### **Vishay Semiconductors**

## Hyperfast Rectifier, 30 A FRED Pt<sup>®</sup>



VS-ETH3006S-M3

VS-ETH3006-1-M3

PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub> 30 A							
V <sub>R</sub>	600 V						
V <sub>F</sub> at I <sub>F</sub>	1.4 V						
t <sub>rr</sub> (typ.)	27 ns						
T <sub>J</sub> max.	175 °C						
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA						
Circuit configuration	Single						

#### **FEATURES**

- · Hyperfast recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Designed and gualified according to JEDEC<sup>®</sup>-JESD 47
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION/APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 95 °C	30	٨	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	180	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-		
	Ň	I <sub>F</sub> = 30 A	-	2.0	2.65	V	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.4	1.8		
	1	V <sub>R</sub> = V <sub>R</sub> rated	-	0.02	30		
Reverse leakage current	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	50	300	μA	
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	20	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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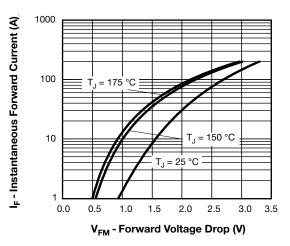
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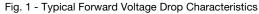


## Vishay Semiconductors

DYNAMIC RECOVERY CH	IARACTER	<b>RISTICS</b> ( $T_J = 25$	°C unless otherwise	specified	I)		
PARAMETER	SYMBOL	TEST (	CONDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 5$	0 A/µs, V <sub>R</sub> = 30 V	-	26	35	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	26	-	ns
		T <sub>J</sub> = 125 °C		-	70	-	
Deels receiver a current		T <sub>J</sub> = 25 °C	$I_{\rm F} = 30  {\rm A}$	-	3.5	-	А
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/µs V <sub>B</sub> = 200 V	-	7.6	-	А
	<u> </u>	T <sub>J</sub> = 25 °C	n	-	50	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	280	-	nC

THERMAL - MECHANIC	AL SPECIFI	CATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	0.95	1.4	°C/W
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	
Thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style D <sup>2</sup> PAK (TO-263AB)	ETH3006S			
Marking device		Case style TO-262AA		ETH3	006-1	





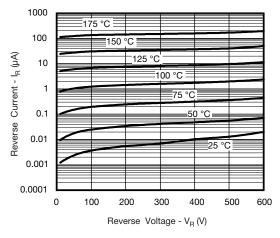


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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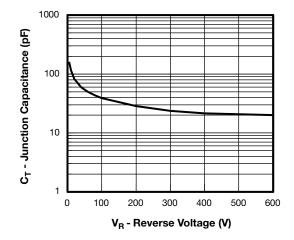


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

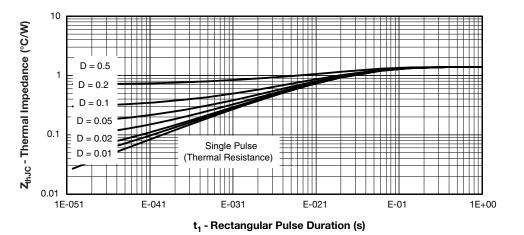


Fig. 4 - Max. Thermal Impedance Z<sub>thJC</sub> Characteristics

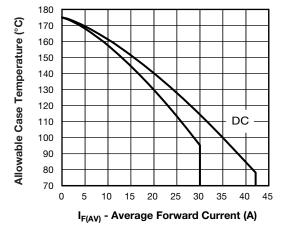


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

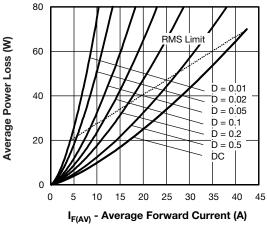


Fig. 6 - Forward Power Loss Characteristics

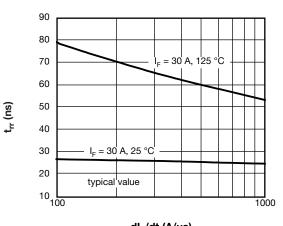
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3

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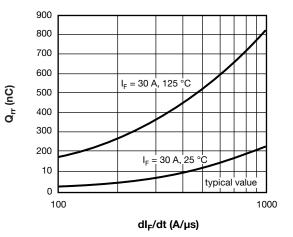


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dl<sub>F</sub>/dt (A/µs)

Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt





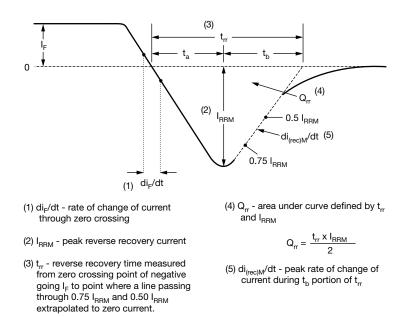


Fig. 9 - Reverse Recovery Waveform and Definitions



## **Vishay Semiconductors**

#### **ORDERING INFORMATION TABLE**

Device code	VS-	Е	т	н	30	06	S	TRL	-M3	
		(2)	(3)	(4)	(5)	(6)		(8)	(9)	
			$\bigcirc$	4	$\bigcirc$	$\bigcirc$	$(\mathbf{r})$	$\bigcirc$	9	
	1 -	- Visl	nay Serr	niconduo	ctors pro	oduct				
	2 -		uit conf	iguratior	۱					
	3 -		single TO-220							
	4 -		hyperfa		erv time	2				
	5 -		rent cod		-					
	6 -		tage coc	-	-					
			= D <sup>2</sup> PAI	•	,					
			= TO-20		,					
	8 -	- • No	one = tu	be (50 p	vieces)					
	ш					oriented	l, for D <sup>2</sup>	PAK (T	O-263AE	3) pa
	-		•						(TO-263/	<i>.</i>
	9 -								ations lea	<i>,</i> .
	3	IVIC	, naiog	,511 1166	,	Somplic	ant, and			

ORDERING INFORMATION (Example)					
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-ETH3006S-M3	50	Antistatic plastic tubes			
VS-ETH3006STRR-M3	800	13" diameter plastic tape and reel			
VS-ETH3006STRL-M3	800	13" diameter plastic tape and reel			
VS-ETH3006-1-M3	50	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS							
Dimensione	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164					
Dimensions	TO-262AA	www.vishay.com/doc?96165					
Port marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96424					

**Vishay Semiconductors** 

D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	MILLIM	MILLIMETERS		INCHES		
STMBOL	MIN.	MAX.	MIN. MAX.		NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INCHES		NOTES		
	STWIDDL	MIN.	MAX.	MIN.	MAX.	NOTES	
	D1	6.86	8.00	0.270	0.315	3	
	E	9.65	10.67	0.380	0.420	2, 3	
	E1	7.90	8.80	0.311	0.346	3	
	е	2.54	2.54 BSC		0.100 BSC		
	Н	14.61	15.88	0.575	0.625		
	L	1.78	2.79	0.070	0.110		
	L1	-	1.65	-	0.066	3	
	L2	1.27	1.78	0.050	0.070		
	L3	0.25	BSC	0.010	BSC		
	L4	4.78	5.28	0.188	0.208		

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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1

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## **Outline Dimensions**



**Vishay Semiconductors** 

**TO-262AA** 

#### **DIMENSIONS** in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

CVMPOI	MILLIN	IETERS	INC	HES	NOTEO
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

 <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis of the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

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1



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